SPECIFICATION

CUSTOMER　:

MODULE NO.:　WG12864D-TMI-NZ#000

APPROVED BY: (FOR CUSTOMER USE ONLY)	
	PCB VERSION:　　　　DATA:

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

ISSUED DATE:

Winstar Display Co., LTD

華凌光電股份有限公司

MODLE NO：

RECORDS OF REVISION			DOC. FIRST ISSUE
VERSION	DATE	REVISED PAGE NO.	SUMMARY
0	2006.05.09		First issue

Contents

1.Module Classification Information

<u>W</u> <u>G</u>　<u>1 2 8 6 4</u>　<u>D</u>　－　<u>T</u> <u>M</u> <u>I</u>　－　<u>NZ#000</u>
① ②　　③　　④　　⑤ ⑥ ⑦　　　　⑧

① Brand：WINSTAR DISPLAY CORPORATION

② Display Type：H→Character Type, G→Graphic Type

③ Display Font： 128 x , 64 dots

④ Model serials no.

⑤ Backlight Type：　　N→Without backlight　　　A→LED, Amber

　　　　　　　　　　　B→EL, Blue green　　　　R→LED, Red

　　　　　　　　　　　D→EL, Green　　　　　　O→LED, Orange

　　　　　　　　　　　W→EL, White　　　　　　G→LED, Green

　　　　　　　　　　　F→CCFL, White　　　　　T→LED, White

　　　　　　　　　　　Y→LED, Yellow Green

⑥ LCD Mode：　　　B→TN Positive, Gray　　　 T→FSTN Negative

　　　　　　　　　　N→TN Negative,

　　　　　　　　　　G→STN Positive, Gray

　　　　　　　　　　Y→STN Positive, Yellow Green

　　　　　　　　　　M→STN Negative, Blue

　　　　　　　　　　F→FSTN Positive

⑦ LCD Polarize　　　A→Reflective, N.T, 6:00　　H→Transflective, W.T,6:00

　Type/ Temperature　D→Reflective, N.T, 12:00　K→Transflective, W.T,12:00

　range/ View　　　　G→Reflective, W. T, 6:00　C→Transmissive, N.T,6:00

　direction　　　　　J→Reflective, W. T, 12:00　F→Transmissive, N.T,12:00

　　　　　　　　　　B→Transflective, N.T,6:00　I→Transmissive, W. T, 6:00

　　　　　　　　　　E→Transflective, N.T.12:00　L→Transmissive, W.T,12:00

⑧ Special Code　　　N : Without　Negative Voltage

　　　　　　　　　　Z:NT7086 driver IC

　　　　　　　　　　00: Sales code　0:version

　　　　　　　　　　#:Fit in with the ROHS Directions and regulations

　　　　　　　　　　Added Touch Panel

2.Precautions in use of LCD Modules

(1)Avoid applying excessive shocks to the module or making any alterations or modifications to it.

(2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD module.

(3)Don't disassemble the LCM.

(4)Don't operate it above the absolute maximum rating.

(5)Don't drop, bend or twist LCM.

(6)Soldering: only to the I/O terminals.

(7)Storage: please storage in anti-static electricity container and clean environment.

3.General Specification

Item	Dimension	Unit
Number of Characters	128 x 64	—
Module dimension	78.0 x 70.0 x 15.4(MAX)	mm
View area	62.0 x 44.0	mm
Active area	56.3 x 38.38	mm
Dot size	0.42 x 0.58	mm
Dot pitch	0.44 x 0.60	mm
LCD type	STN, Negative, Blue , Transmissive	
Duty	1/64	
View direction	6 o'clock	
Backlight Type	LED , White	

4.Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Input Voltage	V_I	V_{SS}	—	V_{DD}	V
Supply Voltage For Logic	$V_{DD}-V_{SS}$	-0.3	—	+7	V
Supply Voltage For LCD	$V_{DD}-V_0$	0	—	15	V
Negative Voltage Output	V_{EE}	—	10	—	V

5.Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	$V_{DD}-V_{SS}$	—	4.75	5.0	5.25	V
Supply Voltage For LCD	$V_{DD}-V_0$	Ta=-20℃	—	—	9.8	V
		Ta=25℃	—	8.5	—	V
		Ta=70℃	7.6	—	—	V
Input High Volt.	V_{IH}	—	V_{DD}-2.2	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	0	—	0.8	V
Output High Volt.	V_{OH}	—	V_{DD}-0.3	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	0	—	0.3	V
Supply Current	I_{DD}	V_{DD}=5V	8.0	10.5	12.	mA

6.Optical Characteristics

ITEM	SYMBAL	CONDITION	MIN.	TYP.	MAX.	UNIT
View Angle	(V) θ	CR$\geqq$2	20	—	40	deg
	(H) φ	CR$\geqq$2	-30	—	30	deg
Contrast Ratio	CR	—	—	3	—	—
Response Time	T rise	—	—	200	300	ms
	T fall	—	—	200	300	ms

6.1 Definitions

■View Angles

Z (Visual angle direction)

θ

LCD

X

ϕ

Y

(Best visual angle direction)

■Contrast Ratio

$$CR = \frac{\text{Brightness at selected state (BS)}}{\text{Brightness at non-selected state (Bns)}}$$

Selected state

Non-selected state

Brightness (%)

Bs

Bns

Operating voltage for LCD driving

■Response Time

Nonselected Condition   Selected Condition   Nonselected Condition

Brightness

100 %

10 %

90 %

tr

Rise Time

td

Decay Time (fall time tf)
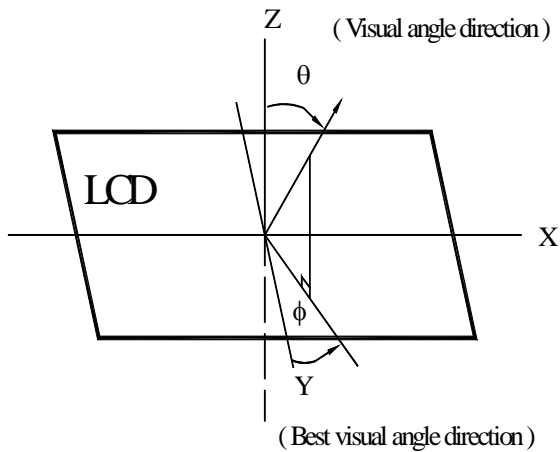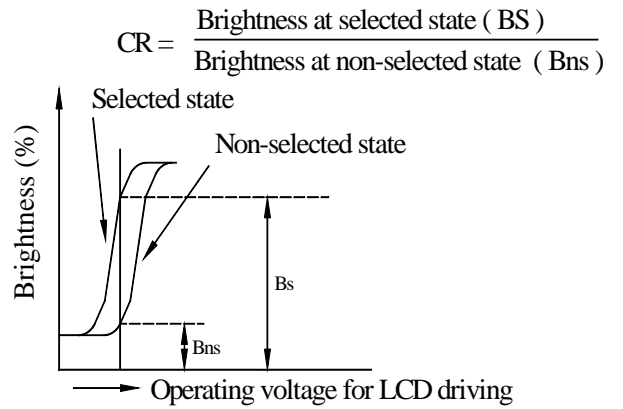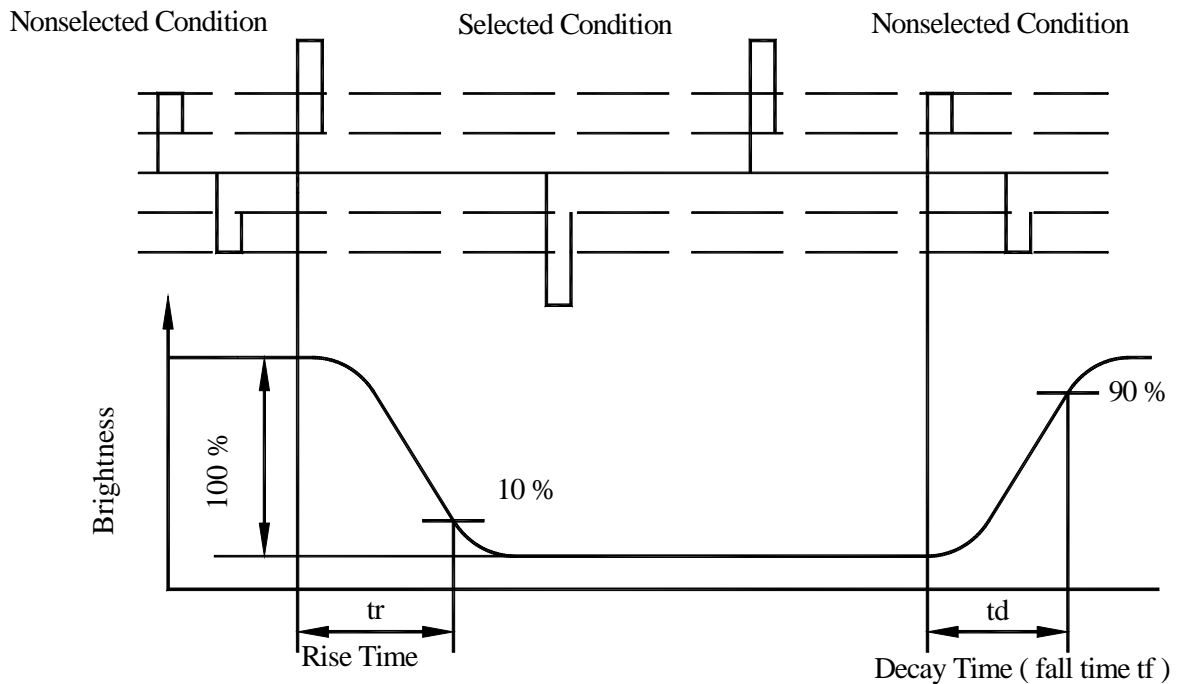
7.Interface Pin Function

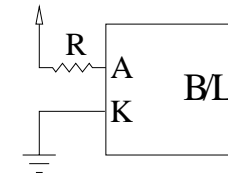| Pin No.	Symbol	Level	Description
1	V_{EE}	-5V	Negative Voltage Output
2	Vss		GND
3	Vdd		Power supply (+5 V)
4	Vo		Power supply for LCD driver
5	WR	L	Data write. Write data into T6963C when WR = L
6	RD	L	Data read. Read data from T6963C when RD = L
7	CE	L	L : Chip enable
8	C/D	H / L	WR=L , C/D=H : Command Write C/D=L: Data write RD=L , C/D=H : Status Read C/D=L: Data read
9	RESET	H / L	H : Normal ; L : Initialize T6963C
10	DB0	H / L	Data bus line
11	DB1	H / L	Data bus line
12	DB2	H / L	Data bus line
13	DB3	H / L	Data bus line
14	DB4	H / L	Data bus line
15	DB5	H / L	Data bus line
16	DB6	H / L	Data bus line
17	DB7	H / L	Data bus line
18	FS	H / L	Pins for selection of font ; H : 6 * 8 , L : 8 * 8
19	K		Power supply for LED B/L -
20	A		Power supply for LED B/L +

8.Contour Drawing & Block Diagram

PIN NO.	SYMBOL
1	FG/VEE
2	Vss
3	Vdd
4	Vo
5	$\overline{WR}$
6	$\overline{RD}$
7	$\overline{CE}$
8	$\overline{CD}$
9	$\overline{RST}$
10	DB0
11	DB1
12	DB2
13	DB3
14	DB4
15	DB5
16	DB6
17	DB7
18	FS
19	K
20	A

78.0±0.5
74.0
68.0
62.0(VA)
61.6(VA)(T.P)
59.0(AA)(TP)
56.3(AA)
P2.54*19=48.26
2.0
20-O1.0 PTH
20-O2.0 PAD
1   20

15.4Max
14.3
9.7

128X64 Dots

LEFT
TOP
BOTTOM
RIGHT

12.9 11.4
44.2(AA)(T.P)
47.2(VA)(T.P)

70.0±0.5
64.92
55.2
44.0(VA)
38.38(AA)
27.5
2.54 7.4 13.0 15.81 7.1
"A"
PIN 4
PIN 1

50.0±1.0
4-φ2.5 PTH
4-φ5.0 PAD

1.1(T.P)
1.6
LED B/L

DETAIL A
SCALE 2/1
(Conductor Rear side)
(Trace Down)
PIN 4
PIN 1
4.0
(Conductor Area)
10.0
(STIFFENER)
1.0
0.65
0.58
5.0±0.15
3.0
(P1.0*3)

PIN NO.	Assignment
1	Right
2	Bottom
3	Left
4	Top

TOUCH PANEL PIN ASSIGENMENT

0.44
0.42
0.6
0.58
DOT SIZE
SCALE 10/1

The non-specified tolerance of dimension is ±0.3mm.

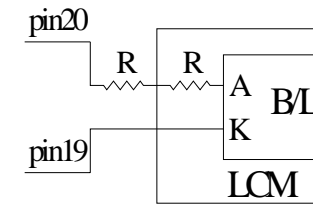| SCALE: 1/1	UNIT: m m	華凌光電股份有限公司
REV: 0	PAGE: 1/1	WINSTAR DISPLAY CO.
APPROVE		MODEL WG12864D-TMI-NZ#000
CHECK		TITLE LCM DRAWING
DRAW	Nina 05/23/06'	DWG NO.

LED B/L Drive Method

1. Drive from A,K

2. Drive from pin19, pin20

3. Drive from Vdd,Vss
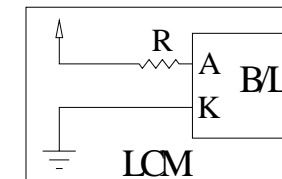
(Contrast performance may go down.)

FS	H	L
Font	6X8	8X8

Font select

External contrast adjustment.

9.Display control instruction

The LCD Module has built in a T6963C LSI controller, It has an 8-bit parallel data bus and control lines for writing or reading through an MPU interface, it has a 128-word character generator ROM (refer to Table 1.), which can control an external display RAM of up to 8K bytes. Allocation of text, graphics and external character generator RAM can be made easily and the display window can be moved freely within the allocated memory range.

•**RAM Interface**

The external RAM is used to store display data(text, graphic and external CG data). It can be freely allocated to the memory area(8 Kbyte max).

Recommend

```
0000H  ┌─────────────┐
       │             │
       │             │
       │  TEXT AREA  │
       │             │
       │             │
0C00H  ├─────────────┤
       │             │
       │  GRAPHIC    │
       │  AREA       │
       │             │
1800H  ├─────────────┤
       │  CG RAM     │
       │  AREA       │
2000H  └─────────────┘
```
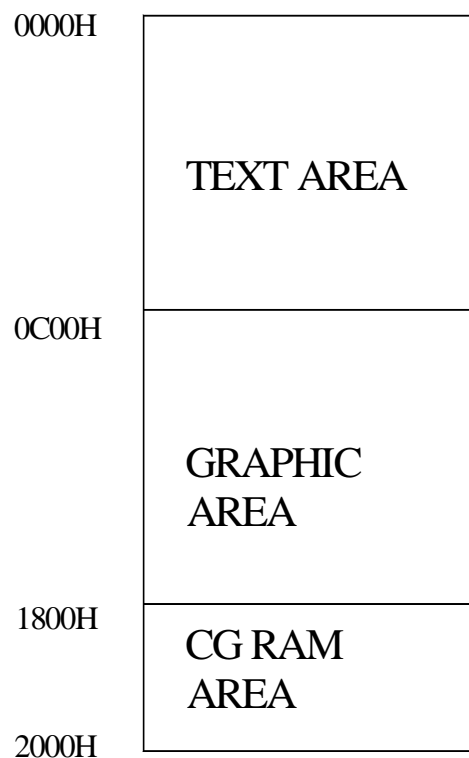
Table 1.

CHARACTER CODE MAP
ROM code 0101

MSB \ LSB	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0		!	"	#	$	%	&	'	(	)	*	+	,	-	.	/
1	0	1	2	3	4	5	6	7	8	9	:	;	<	=	>	?
2	@	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O
3	P	Q	R	S	T	U	V	W	X	Y	Z	[	\	]	^	_
4	`	a	b	c	d	e	f	g	h	i	j	k	l	m	n	o
5	p	q	r	s	t	u	v	w	x	y	z	{	|	}	~	
6	Ş	ü	é	â	ä	à	å	ç	ê	ë	è	ï	î	ì	Ä	Å
7	É	æ	Æ	ö	ö	ö	ü	ü	ÿ	Ö	Ü	¢	£	¥	₨	ƒ

・Flowchart of communications with MPU

(1)Status Read

A status check must be performed before data is read or written.

Status check

The Status of T6963C can be read from the data lines.

$\overline{RD}$	L
$\overline{WR}$	H
$\overline{CE}$	L
C/D	H
Do to D7	H

The T6963C status word format is as follows:

MSB   LSB

STA7	STA6	STA5	STA4	STA3	STA2	STA1	STA0
D7	D6	D5	D4	D3	D2	D1	D0

STA0	Check command execution capability	0:Disable 1:Enable
STA1	Check data read/write Capability	0:Disable 1:Enable
STA2	Check Auto mode data read capability	0:Disable 1:Enable
STA3	Check Auto mode data write capability	0:Disable 1:Enable
STA4	Not used	
STA5	Check controller operation capability	0:Disable 1:Enable
STA6	Error flag. Used for Screen Peek and Screen copy commands.	0:No error 1:Error
STA7	Check the blink condition	0:Disable off 1:Normal display

(Note 1) It is necessary to check STA0 and STA1 at the same time.

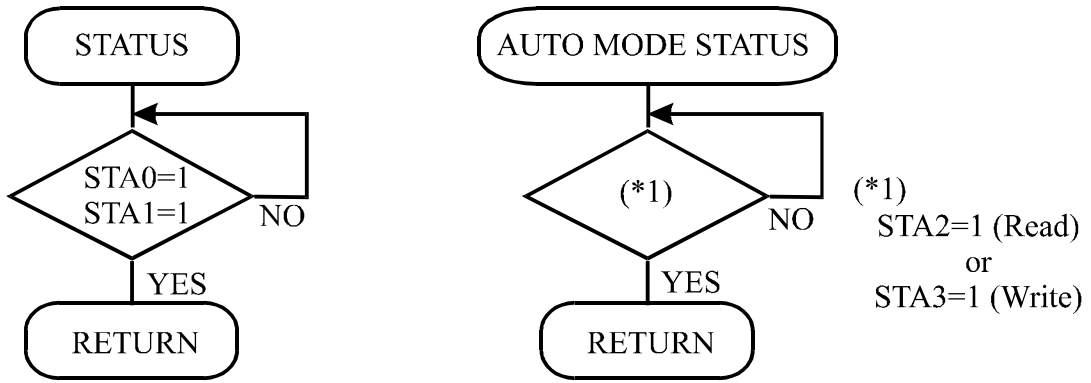There is a possibility of erroneous operation due to a hardware interrupt.

(Note 2) For most modes STA0/STA1 are used as a status check.

(Note 3) STA2 and STA3 are valid in Auto mode; STA0 and STA1 are invalid.

Status Checking flow

(a)   (b)

```
         STATUS                    AUTO MODE STATUS

        ╱ STA0=1 ╲                    ╱        ╲
        ╲ STA1=1 ╱ ──NO              ╲  (*1)  ╱ ──NO        (*1)
                                                          STA2=1 (Read)
          │YES                         │YES                    or
        RETURN                        RETURN              STA3=1 (Write)
```

(Note 4) When using the MSB=0 command, a Status Read must be performed.

If a status check is not carried out, the T6963C cannot operate normally, even after a

delay time.

The hardware interrupt occurs during the address calculation period (at the end of

each line).

If a MSB=0 command is sent to the T6963C during this period, the T6963C enters

Wait status.

If a status check is not carried out in this state before the next command is sent, there

is the possibility that the command or data date will not be received.

(2)Setting date

When using the T6963C, first set the data, then set the command.

Procedure for sending a command

(a)The case of 1 date   (b)The case of 2 data

```
    ( send command )                    ( send command )
           |                                   |
   [| Status check |]  STA0, 1         [| Status check |]  STA0, 1
           |                                   |
   [   Data write   ]                  [   Data write   ]
           |                                   |
   [| Status check |]                  [| Status check |]
           |                                   |
   [ Command write  ]                  [   Date write   ]
           |                                   |
      (   END   )                       [ Status check  ]
                                                |
                                        [ Command write ]
                                                |
                                           (   END   )
```
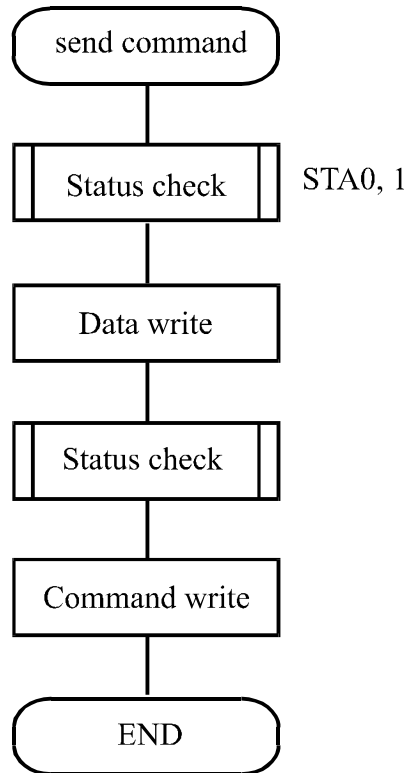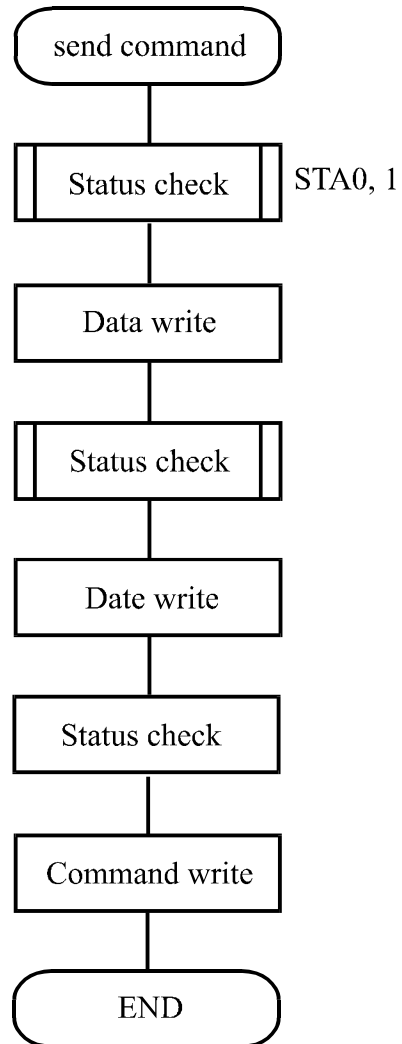
(Note)   When sending more than two data, the last datum (or last two data)is valid.

・COMMAND DEFINITIONS

COMMAND	CODE	D1	D2	FUNCTION
REGISTERS SETTING	00100001 00100010 00100100	X address Date Low address	Y address 00H High address	Set Cursor Pointer Set Offset Register Set Address Pointer
SET CONTROL WORD	01000000 01000001 01000010 01000011	Low address Columns Low address Columns	High address 00H High address 00H	Set Text Home Address Set Text Area Set Graphic Home Address Set Graphic Area
MODE SET	1000x000 1000x001 1000x011 1000x100 10000xxx 10001xxx	— — — — — —	— — — — — —	OR mode EXOR mode AND mode Text Attribute mode Internal CG ROM mode External CG RAM mode
DISPLAY MODE	10010000 1001xx10 10001xx11 100101xx 100110xx 100111xx	— — — — — —	— — — — — —	Display off Cursor on, blink off Cursor on, blink on Text on, graphic off Text off, graphic on Text on, graphic on
CURSOR PATTERN SELECT	10100000 10100001 10100010 10100011 10100100 10100101 10100110 10100111	— — — — — — — —	— — — — — — — —	1-line cursor 2-line cursor 3-line cursor 4-line cursor 5-line cursor 6-line cursor 7-line cursor 8-line cursor
DATA AUTO READ/WRITE	10110000 10110001 10110010	— — —	— — —	Set Data Auto Write Set Data Auto Read Auto Reset
DATA READ/WRITE	11000000 11000001 11000010 11000011 11000100 11000101	Data — Data — Data —	— — — — — —	Data Write and Increment ADP Data Read and Increment ADP Data Write and Decrement ADP Data Read and Decrement ADP Data Write and Nonvariable ADP Data Read and Nonvariable ADP
SCREEN PEEK	11100000	—	—	Screen Peek

X : invalid

COMMAND	CODE	D1	D2	FUNCTION
SCREEN COPY	11101000			Screen Copy
BIT SET/RESET	11110xxx 11111xxx 1111x000 11111x001 11111x010 11111x011 11111x100 11111x101 11111x110 11111x111	— — — — — — — — — —	— — — — — — — — — —	Bit Reset Bit Set Bit 0 (LSB) Bit 1 Bit 2 Bit 3 Bit 4 Bit 5 Bit 6 Bit 7 (MSB)

X : invalid

・Setting registers

CODE	HEX.	FUNCTION	D1	D2
00100001	21H	SET CURSOR POINTER	X ADRS	Y ADRS
00100010	23H	SET OFFSET REGISTER	DATA	00H
00100100	24H	SET ADDRESS POINTER	LOW ADRS	HIGH ADRS

(1)Set Cursor Pointer

The position of the cursor is specified by X ADRS and Y ADRS. The cursor position can only

be moved by this command. Data read/write from the MPU never changes the cursor pointer.

X ADRS and Y ADRS are specified as follows.

X ADRS   00H to 4FH (lower 7 bits are valid)

Y ADRS   00H to 1FH (lower 5 bits are valid)

Single-Scan

X ADRS 00 to 4FH

Y ADRS 00H to 0FH

(2)Set Offset Register

The offset register is used to determine the external character generator RAM area.

The T6963C has a 16-bit address bus as follows.

MSB   LSB

ad15	ad14	ad13	ad12	ad11	ad10	ad9	ad8	ad7	ad6	ad5	ad4	ad3	ad2	ad1	ad0

Offset Register Data   Character Code   Line Scan
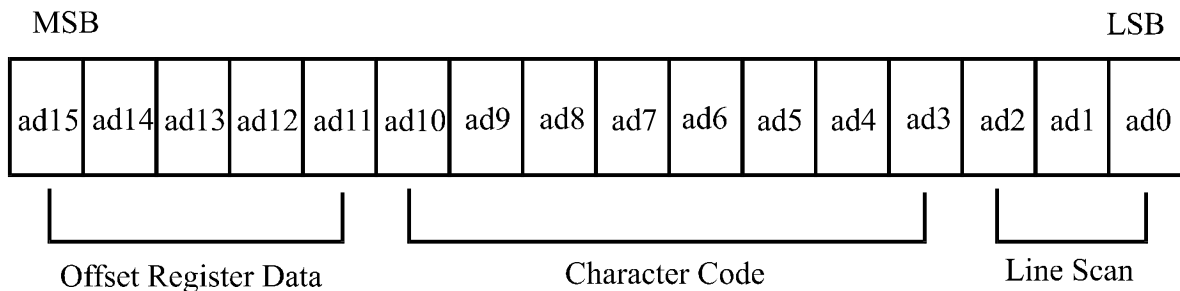
T6963C assign External character generator, when character code set 80H TO FFH in using

internal character generator. Character code 00H to 80H assign External character generator,

when External generator mode.

The senior five bits define the start address in external memory of the CG RAM area. The next

eight bits represent the character code of the character. In internal CG ROM, character codes

00H to 7FH represent the predefined "internal" CG ROM characters, and codes 80H to FFH

represent the user's own "external" characters. In external CG ROM mode, all 256 codes from

00H to FFH can be used to represent the user's own characters. The three least significant bits
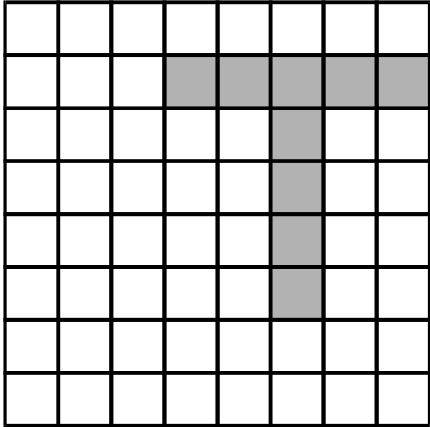
indicate one of the eight rows of eight dots that define the character's shape.

<u>The relationship between display RAM address and offset register</u>

Offset register data	CG RAM hex. address (start to end)
00000	0000 to 07 FFH
00001	0800 to 0FFFH
00010	1000 to 17FFH
11100	E000 to E7FFH
11101	E800 to EFFFH
11110	F000 to F7FFH
11111	F800 to FFFFH

(Example 1)

Offset register	02H				
Character code	80H				
Character generator RAM start address	0001	0100	0000	0000	
	1	4	0	0	H

(address)	(data)
1400H	00H
1401H	1FH
1402H	04H
1403H	04H
1404H	04H
1405H	04H
1406H	04H
1407H	00H

(Example 2) The relationship between display RAM data and display characters

(RAM DATA)	(Character)
AB γ DE ζ GHIJKLM	
21H	A
22H	B
83H	γ
24H	D
25H	E
86H	ζ

Display character
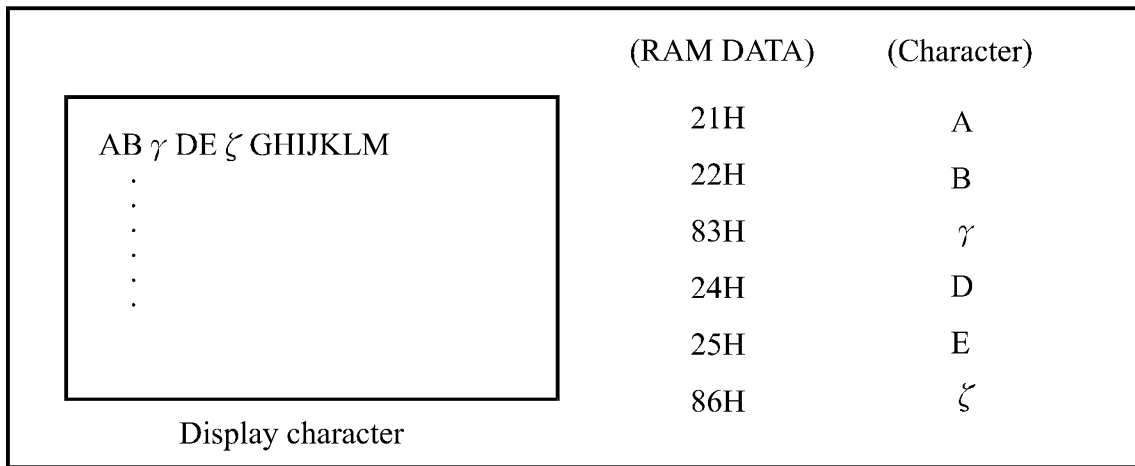
γ and ζ are displayed by character generator RAM.

(3)Set Address Pointer

The Set Address Pointer command is used to indicate the start address for writing to (or reading from)external RAM.

The Flowchart for Set Address Pointer command

```
   ┌──────────────────────┐
   │  Set Address Pointer │
   └──────────────────────┘
              │
   ┌──────────────────────┐
   │    Status check      │  STA0, 1
   └──────────────────────┘
              │
   ┌──────────────────────┐
   │   Set Address data   │
   │    (lower 8 bits)     │
   └──────────────────────┘
              │
   ┌──────────────────────┐
   │    Status check      │
   └──────────────────────┘
              │
   ┌──────────────────────┐
   │   Set Address data   │
   │    (lower 8 bits)     │
   └──────────────────────┘
              │
   ┌──────────────────────┐
   │    Status check      │
   └──────────────────────┘
              │
   ┌──────────────────────┐
   │  Send command 24H    │  Send Set Address Pointer command
   └──────────────────────┘
              │
   ┌──────────────────────┐
   │        END           │
   └──────────────────────┘
```
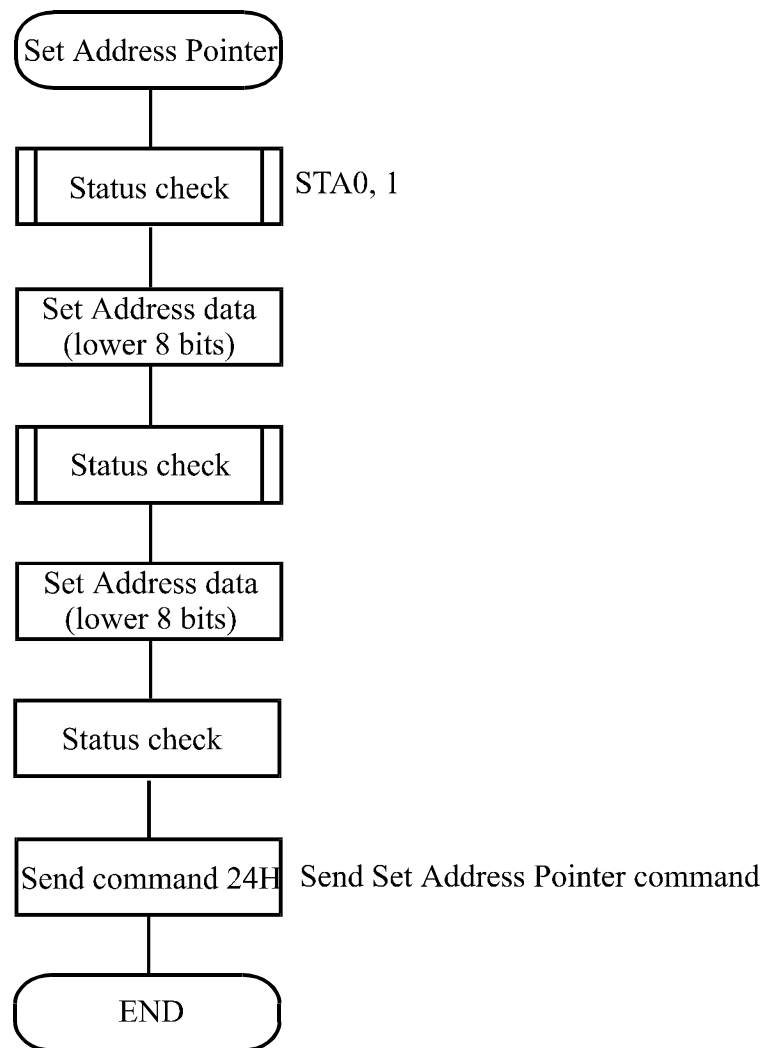
・Set Control Word

CODE	HEX.	FUNCTION	D1	D2
01000000	40H	Set Text Home Address	Low address	High address
01000001	41H	Set Text Area	Columns	00H
01000010	42H	Set Graphic Home Address	Low address	High address
01000011	43H	Set Graphic Area	Columns	00H

The home address and column size are defined by this command.

(1)Set Text Home Address

The starting address in the external display RAM for text display is defined by this command.

The text home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

TH		TH+CL
TH+TA		TH+TA+CL
(TH+TA)+TA		TH+2TA+CL
(TH+2TA)+TA		TH+3TA+CL
TH+(n-1)TA		TH+(n-1)TA+CL

TH:Text home address

TA:Text area number (columns)

CL:Columns are fixed by hardware (pin-programmable).

(Example)

Text home address ：0000H

Text area ：0020H

：32 Columns

：4 Lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	002FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH

(2)Set Graphic Home Address

The starting address of the external display RAM used for graphic display is defined by this

command. The graphic home address indicates the leftmost and uppermost position.

The relationship between external display RAM address and display position

GH		GH+GL
GH+GA		GH+GA+CL
(GH+GA)+GA		GH+2GA+CL
(GH+2GA)+GA		GH+3GA+CL
GH+(n-1)GA		GH+(n-1)GA+CL

GH:Graphic home address

GA:Graphic area number (columns)

CL:Columns are fixed by hardware (pin-programmable).

(Example)

Graphic home address ：0000H

Graphic area ：0020H

：32 Columns

：2 Lines

0000H	0001H		001EH	001FH
0020H	0021H		003EH	003FH
0040H	0041H		005EH	005FH
0060H	0061H		007EH	007FH
0080H	0081H		009EH	009FH
00A0H	00A1H		00BEH	00BFH
00C0H	00C1H		00DEH	00DFH
00E0H	00E1H		00FEH	00FFH
0100H	0101H		011EH	011FH
0120H	0121H		013EH	013FH
0140H	0141H		015EH	014FH
0160H	0161H		017EH	017FH
0180H	0181H		109EH	019FH
01A0H	01A1H		01BEH	01BFH
01C0H	01C1H		01DEH	01DFH
01E0H	01E1H		01FEH	01FFH

(3)Set Text Area

The display columns are defined by the hardware Setting. This command can be used to adjust the columns of the display.

(Example)

LCD size	20 columns, 4lines
Text home address	0000H
Text area	0014H

Set 32 columns, 4 Lines

0000	0001	········	0013	0014	········	001F
0014	0015	········	0027	0028	········	0033
0028	0029	········	003B	003C	········	0047
003C	003D	········	004F	0050	········	005B

|←————————→LCD←————————|

(4)Set Graphic Area

The display columns are defined by the hardware setting. This command can be used to adjust the columns of the graphic display.

(Example)

LCD size	20 columns, 2lines
Graphic home address	：0000H
Graphic are	：0014H

Set 32 columns, 2 Lines

0000	0001	………	0013	0014	………	001F
0014	0015	………	0027	0028	………	0033
0028	0029	………	003B	003C	………	0047
003C	003D	………	004F	0050	………	005B
0050	0051	………	0063	0064	………	006F
0064	0065	………	0077	0078	………	0083
0078	0079	………	008B	008C	………	0097
008C	008D	………	009F	00A0	………	00AB
00A0	00A1	………	00B3	00B4	………	00BF
00B4	00B5	………	00C7	00C8	………	00D3
00C8	00C9	………	00DB	00DC	………	00E7
00DC	00DD	………	00EF	00F0	………	00FD
00F0	00F1	………	0103	0104	………	011F
0104	0105	………	0127	0128	………	0123
0128	0129	………	013B	0013C	………	00147
013C	013D	………	014F	0150	………	015B

⟶ LCD ◄

If the graphic area setting is set to match the desired number of columns on the LCD, the addressing scheme will be automatically modified so that the start address of each line equals the end address of the previous line +1.

・Mode set

CODE	FUNCTION	OPERAND
1000×000	OR Mode	—
1000×001	EXOR Mode	—
1000×011	AND Mode	—
1000×100	TEXT ATTRIBUTE Mode	—
10000×××	Internal Character Generator Mode	—
10001×××	External Character Generator Mode	—

X:invalid

The display mode is defined by this command. The display mode does not change until the next command is sent. The logical OR, EXOR, AND of text or graphic display can be displayed. In Internal Character Generator mode, character codes 00H to 7FH are assigned to the built-in character generator ROM. The character codes 80H to FFH are automatically assigned to the external character generator RAM.

(Example)
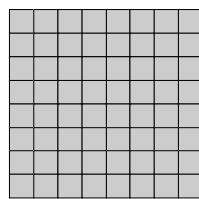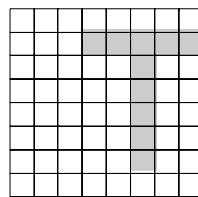
GRAPHIC   TEXT
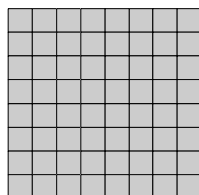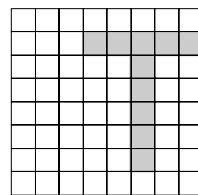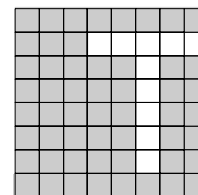
"OR"   "AND"   "TXOR"

(Note)Attribute functions can only be applied to text display, since the attribute data is placed in the graphic RAM area.

Attribute function

The attribute operations are Reverse display, Character blink and Inhibit. The attribute data is written into the graphic area which was defined by the Set Control Word command. Only text display is possible in Attribute Function mode; graphic display is automatically disabled. However, the Display Mode command must be used to turn both Text and Graphic on in order for the Attribute function to be available.

The attribute data for each character in the text area is written to the same address in the graphic

area. The Attribute function is defined as follows.

Attribute RAM 1byte

×	×	×	×	d3	d2	d1	d0

d3	d2	d1	d0	FUNCTION
0	0	0	0	Normal display
0	1	0	1	Reverse display
0	0	1	1	Inhibit display
1	0	0	0	Blink of normal display
1	1	0	1	Blink of reverse display
1	0	1	1	Blink of inhibit display

X:invalid

・Display mode

CODE	FUNCTION	OPERAND
10010000	Display off	－
1001××10	Cursor on, blink off	－
1001××11	Cursor on, blink on	－
100101××	Text on, graphic off	－
100110××	Text off, graphic on	－
100111××	Text on, graphic on	－

X:invalid

1	0	0	1	D3	D2	D1	D0

Cursor blink   on:1, off:0

Cursor display   on:1, off:0

Text display   on:1, off:0

Graphic display   on:1, off:0
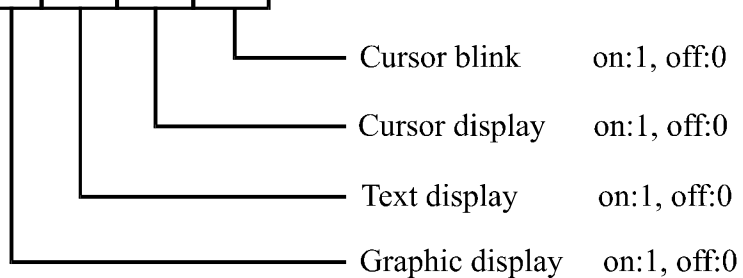
(Note)It is necessary to turn on "Text display" and "Graphic display" in the following cases.

a)Combination of text/graphic display

b)Attribute function

・Cursor pattern select

CODE	FUNCTION	OPERAND
10100000	1-line cursor	—
10100001	2-line cursor	—
10100010	3-line cursor	—
10100011	4-line cursor	—
10100100	5-line cursor	—
10100101	6-line cursor	—
10100110	7-line cursor	—
10100111	8-line cursor	—

When cursor display is ON, this command selects the cursor pattern in the range 1 line to 8 lines.

The cursor address is defined by the Cursor Pointer Set command.
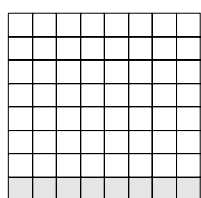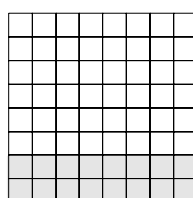
1-line cursor   2-line cursor   8-line cursor
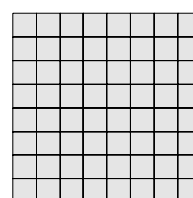
・Data Auto Read/Write

CODE	HEX.	FUNCTION	OPERAND
10110000	B0H	Set Data Auto Write	—
1011001	B1H	Set Data Auto Read	—
10110010	B2H	Auto Reset	—

The command is convenient for sending a full screen of data from the external display RAM.

After setting Auto mode, a Data Write (or Read) command is need not be sent between each datum. A Data Auto Write (or Read) command must be sent after a Set Address Pointer command.

After this command, the address pointer is automatically incremented by 1 after each datum. In Auto mode, the T6963C cannot accept any other commands.

The Auto Reset command must be sent to the T69963C after all data has been sent, to clear Auto mode.

(Note)A Status check for Auto mode

(STA2, STA3 should be checked between sending of each datum. Auto Reset should be performed after checking STA3=1 (STA2=1.) Refer to the following flowchart.

a)Auto Read mode

b)Auto Write mode

```
      Auto read start
             |
      Set Address Pointer
             |
      Status check 1        STA0, 1
             |
      Auto read B1H
             |
      Status check 2        STA2
             |
      Data read
             |
      Status check 2    ┐
             |          ├ Repetition
      Data read         ┘
             |
      status check 2
             |
      Auto reset B2H
             |
           END
```

```
      Auto write start
             |
      Set Address Pointer
             |
      Status check 1        STA0, 1
             |
      Auto read B0H
             |
      Status check 2        STA3
             |
      Data write
             |
      Status check 2    ┐
             |          ├ Repetition
      Data write        ┘
             |
      status check 2
             |
      Auto reset B2H
             |
           END
```

```
┌─────────────────────┐
│ Set Address Pointer │
└─────────────────────┘
          │
  ║ Status check 1 ║      STA0,1          ( Status check 1 )      ( Status check 2 )
          │                                      │                        │
┌─────────────────────┐                          ▼                        ▼
│  Set Address data   │                 NO  ╱ STA0=1 ╲         NO  ╱ STA2=1 ╲
│   (lower 8 bits)    │                 ◄──╱  STA1=1? ╲        ◄──╱  STA3=1? ╲
└─────────────────────┘                     ╲        ╱             ╲        ╱
          │                                   ╲     ╱                ╲     ╱
  ║ Status check 1 ║                            │ YES                  │ YES
          │                              ( RETURN )             ( RETURN )
┌─────────────────────┐
│  Set Address data   │
│   (upper 8 bits)    │
└─────────────────────┘
          │
  ║ Status check 1 ║
          │
┌─────────────────────┐
│ Set Address Pointer │   Send Set Address Pointer command
└─────────────────────┘
          │
       ( END )
```

・ Date Read/Write

CODE	HEX.	FUNCTION	OPERAND
11000000	C0H	Data Write and Increment ADP	Data
11000001	C1H	Data Read and Increment ADP	—
11000010	C2H	Data Write and Decrement ADP	Data
11000011	C3H	Data Read and Decrement ADP	—
11000100	C4H	Data Write and Nonvariable ADP	Data
11000101	C5H	Data Read and Nonvariable ADP	—

This command is used for writing data from the MPU to external display RAM, and reading data from external display RAM to the MPU. Data Write/Data Read should be executed after setting address using Set Address Pointer command. The address pointer can be automatically incremented or decremented using this command.

(Note)This command is necessary for each 1-byte datum.

Refer to the following flowchart.

Data write start

Set Address Pointer — (Example) Address pointer=1000H

Status check 1 — STA0, 1

Set write data — (Example) Data=AAH

Status check 1

Data write C0H — AAH is written in 1000H address. Address pointer is 1001H.

END
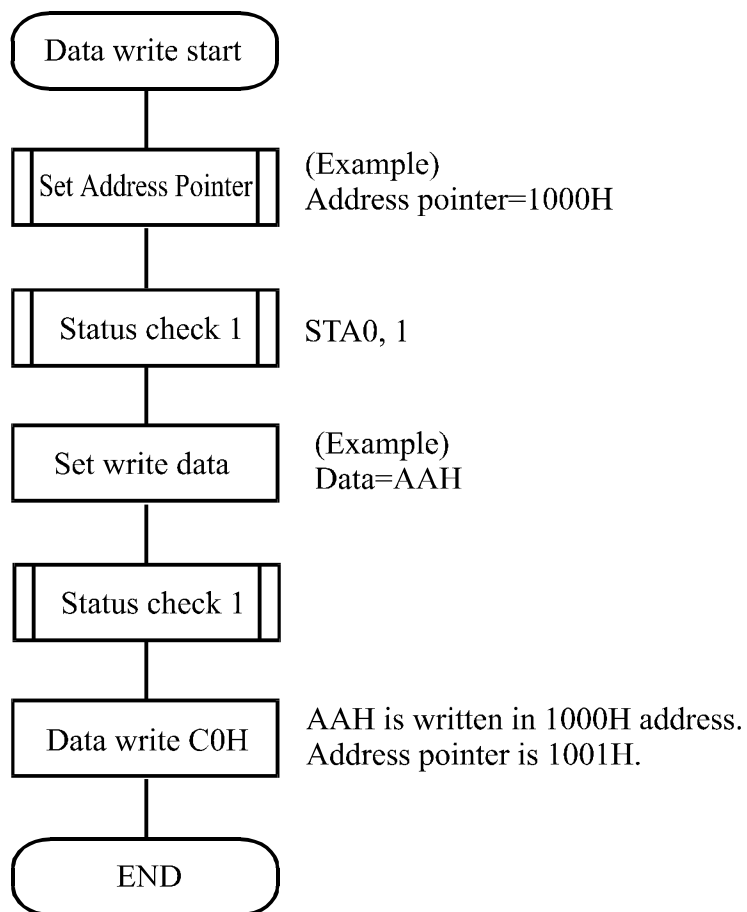
・Screen Peek

CODE	HEX.	FUNCTION	OPERAND
11100000	E0H	Screen Peek	－e

This command is used to transfer 1 byte of displayed data to the data stack; this byte can then be read from the MPU by data access. The logical combination of text and graphic display data on the LCD screen can be read by this command.

The status (STA6) should be checked just after the Screen Peek command. If the address determined by the Set Address Pointer command is not in the graphic area, this commands is ignored and a status flag (STA6) is set.

Refer to the following flowchart.

Screen peek start

Set Address Pointer

Status check 1 — STA0, 1

Screen peek E0H

NO — (*)

(*) Status check STA6=0?

YES

Status check 1

Data access

END
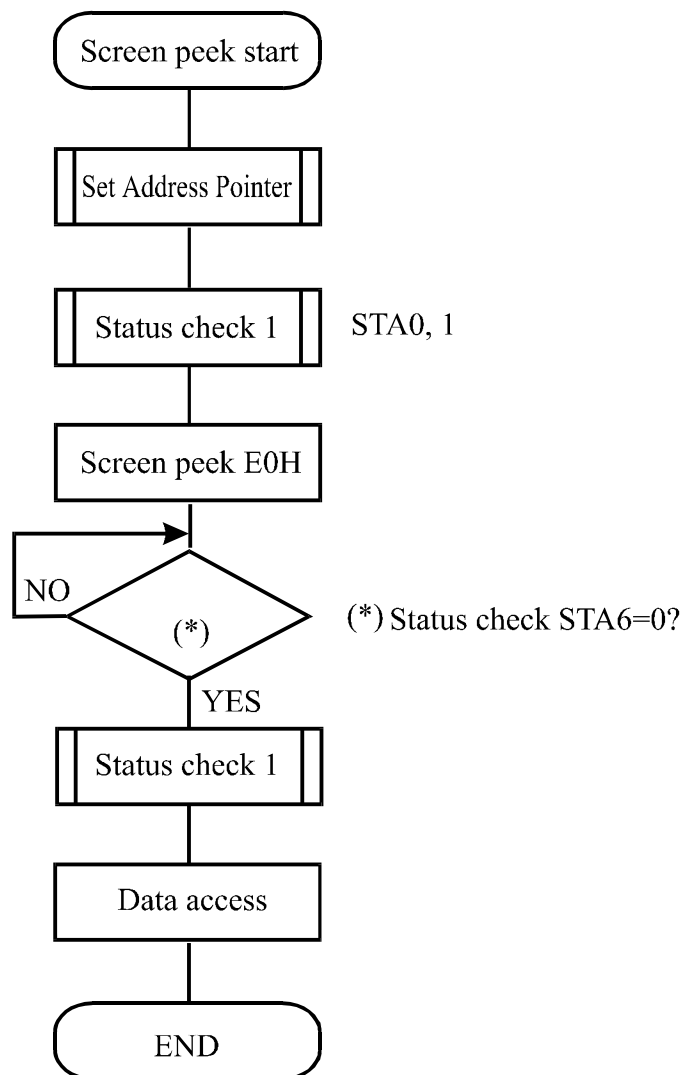
・Screen Copy

CODE	HEX.	FUNCTION	OPERAND
11101000	E8H	Screen Copy	─

This command copies a single raster line of data to the graphic area.

The start point must be set using the Set Address Pointer command.

(Note 1)   If the attribute function is being used, this command is not available.

(With Attribute data is graphic area data.)

Refer to the following flowchart.

```
          ┌─────────────────────┐
          │  Screen copy start  │
          └─────────────────────┘
                     │
          ┌──────────▼──────────┐
     ┌───▶│ Set Address Pointer │
     │    └─────────────────────┘
     │               │
     │    ┌──────────▼──────────┐
     │    │   Status check 1    │      STA0,1
     │    └─────────────────────┘
     │               │
     │    ┌──────────▼──────────┐
     │    │  Screen copy E8H    │
     │    └─────────────────────┘
     │               │
     │              ◄┼──────────────┐
     │             ╱ ╲              │
  YES│            ╱   ╲             │
     └──────────◄(*1)  ►            │
                 ╲    ╱             │
                  ╲  ╱              │
                   ╲╱               │
                 NO │               │
                   ╱╲               │
                  ╱  ╲          NO  │
                 ◄(*2) ►───────────┘
                  ╲   ╱
                   ╲ ╱
                YES │
          ┌─────────▼─────────┐
          │        END        │
          └───────────────────┘
```

(*1)Status check STA6=1
(*2)Status check STA0/1=1
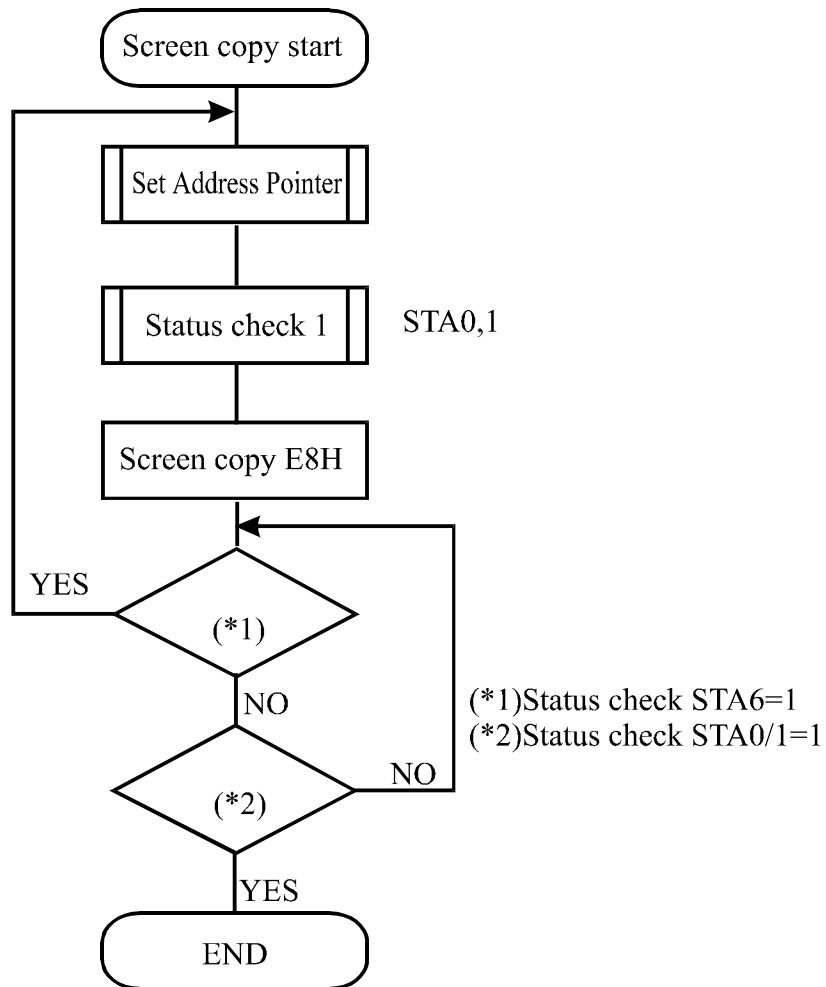
・Bit Set/Reset

CODE	FUNCTION	OPERAND	
11110xxx	Bit Reset	—	
11111xxx	Bit Set	—	
1111x000	Bit 0 (LSB)	—	
1111x001	Bit 1	—	
1111x010	Bit 2	—	
1111x011	Bit 3	—	
1111x100	Bit 4	—	
1111x101	Bit 5	—	
1111x110	Bit 6	—	X:invalid
1111x111	Bit 7 (MSB)	—	

This command use to set or reset a bit of the byte specified by the address pointer.

Only one bit can be set/reset at a time.

Refer to the following flowchart.

```
           ┌─────────────────┐
           │  Bit Set/Reset  │
           └─────────────────┘
                    │
         ┌────────────────────┐
         │ Set Address Pointer │
         └────────────────────┘
                    │
         ┌────────────────────┐
         │  Status check 1    │   STA0,1
         └────────────────────┘
                    │
         ┌────────────────────┐
         │   Set write data   │
         └────────────────────┘
                    │
         ┌────────────────────┐
         │  Status check 1    │
         └────────────────────┘
                    │
         ┌────────────────────┐
         │  Bit Set (reset)   │
         └────────────────────┘
                    │
           ┌─────────────────┐
           │       END       │
           └─────────────────┘
```
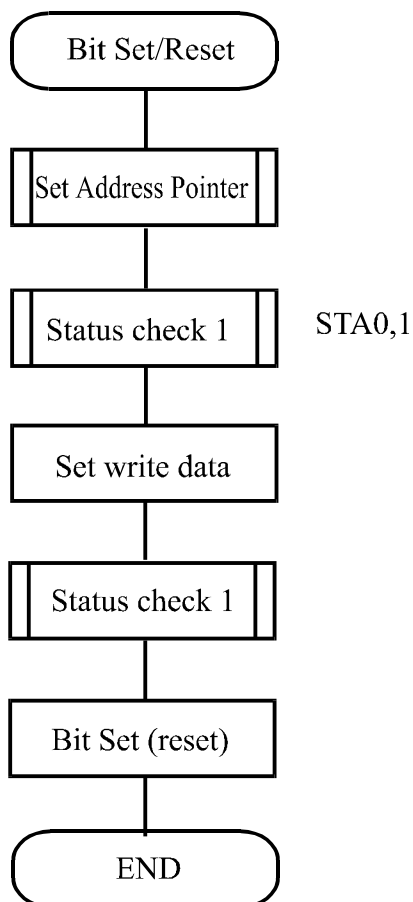
10.Timing Characteristics

Bus Timing
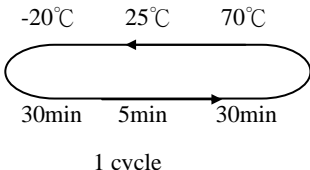
(Vss = 0 V , VDD = 5 V)

Item	Symbol	Min	Typ	Max	Unit
C/D Set-up Time	tCDS	100	—	—	ns
C/D Hold Time	tCDH	10	—	—	ns
CE,RD,WR Pulse Width	tCDS,tRD,tWR	80	—	—	ns
Data Set-up Time	tDS	80	—	—	ns
Data Hold Time	tDH	40	—	—	ns
Access Time	tACC	—	—	150	ns
Output Hold Time	tOH	10	—	50	ns
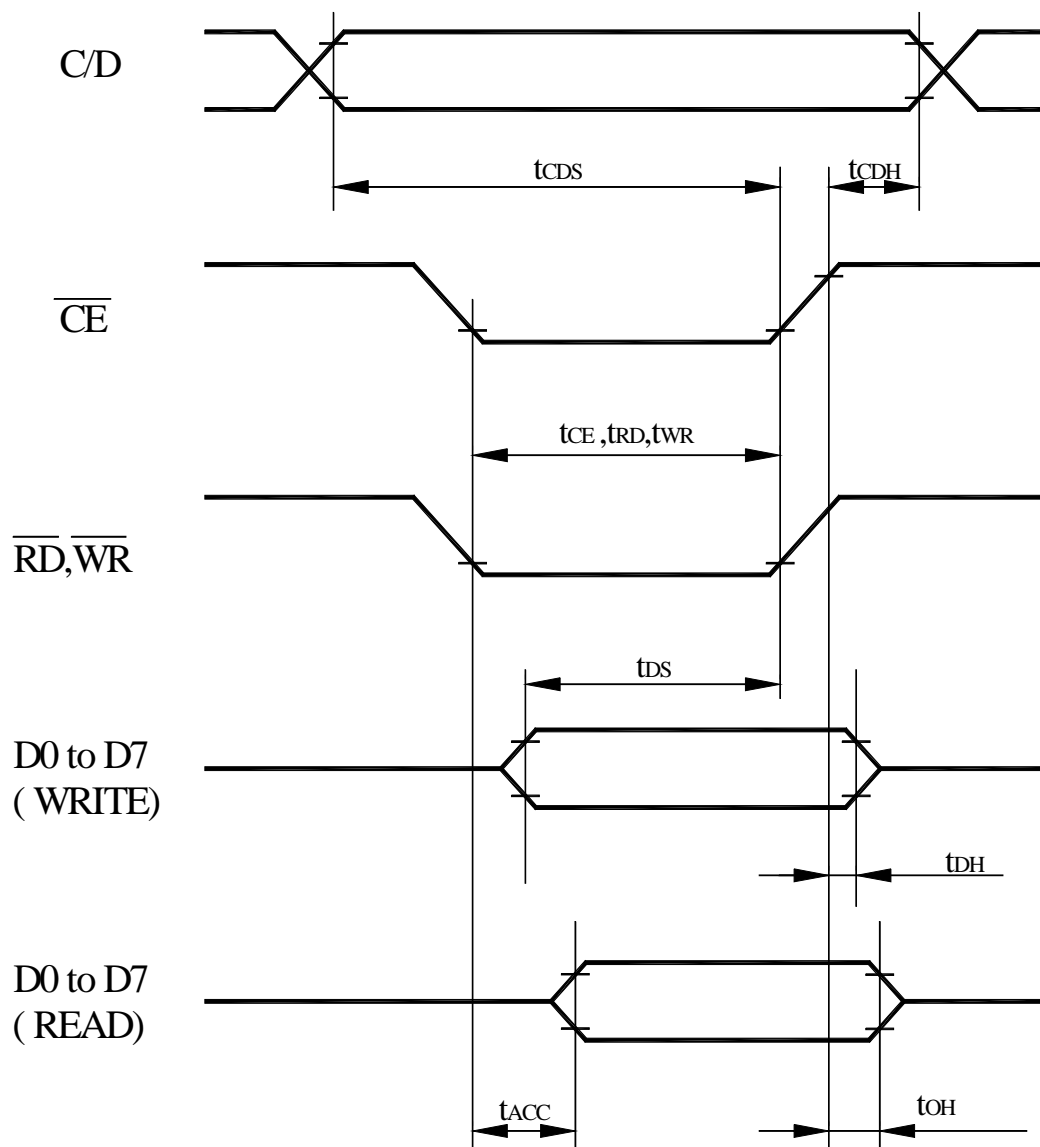
11.RELIABILITY

Content of Reliability Test (wide temperature, -20℃~70℃)

Environmental Test			
Test Item	**Content of Test**	**Test Condition**	**Note**
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80℃ 200hrs	2
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30℃ 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70℃ 200hrs	___
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20℃ 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60℃,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60℃,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20℃　　25℃　　70℃ 30min　5min　30min 1 cycle	-20℃/70℃ 10 cycles	___
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	___

Note1: No dew condensation to be observed.
Note2: The function test shall be conducted after 4 hours storage at the normal
Temperature and humidity after remove from the test chamber.
Note3: Vibration test will be conducted to the product itself without putting it in a container.
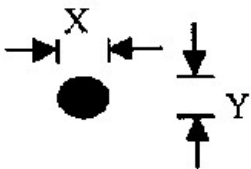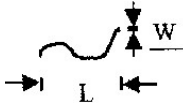
12.Backlight Information
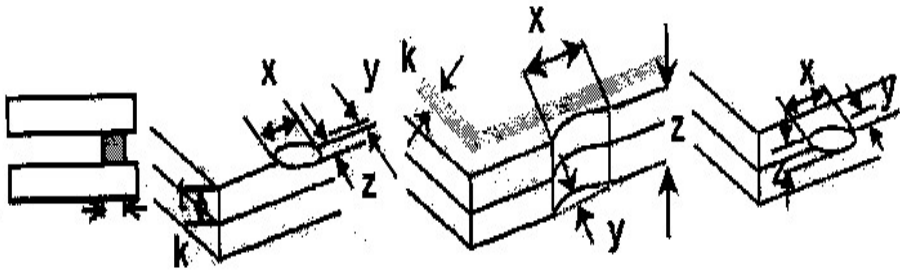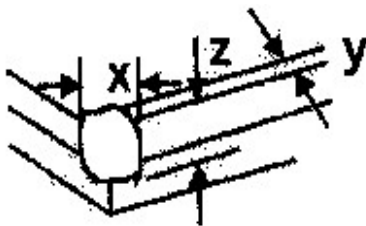
Specification
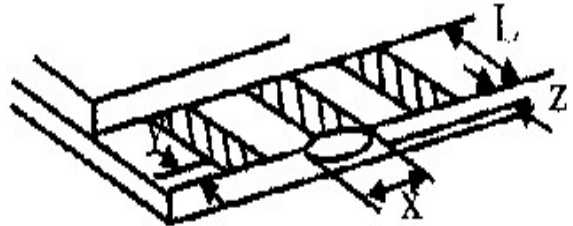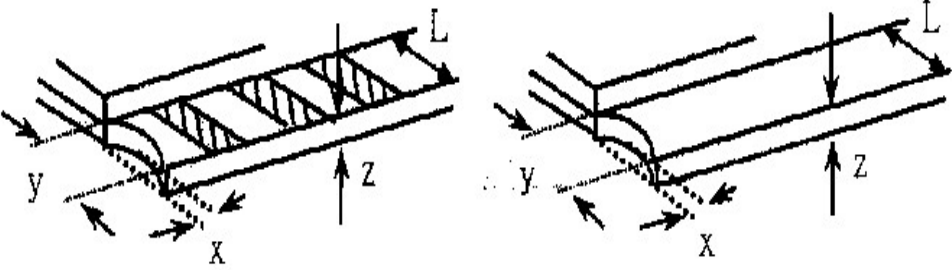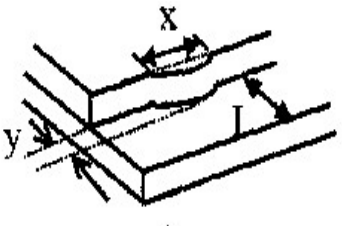
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	120	160	200	mA	V=3.6V
Supply Voltage	V	3.4	3.6	3.8	V	
Reverse Voltage	VR	—	—	5	V	
Luminous Intensity	IV	240	300	—	CD/M^2	ILED=160mA
Life Time		—	50K	—	Hr.	ILED$\leqq$160mA
Color	White					
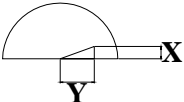
Note: The LED of B/L is drive by current only, drive voltage is for reference only.

drive voltage can make driving current under safety area (current between

minimum and maximum).

13. Inspection specification

NO	Item	Criterion	AQL
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leqq$0.25mm, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing $\Phi=(x + y) / 2$ | SIZE	Acceptable Q TY
---	---
$\Phi \leqq 0.10$	Accept no dense
$0.10 < \Phi \leqq 0.20$	2
$0.20 < \Phi \leqq 0.25$	1
$0.25 < \Phi$	0		2.5

3.2 Line type table (row 03 continued):

Length	Width	Acceptable Q TY
---	W$\leqq$0.02	Accept no dense
L$\leqq$3.0	0.02<W$\leqq$0.03	2
L$\leqq$2.5	0.03<W$\leqq$0.05	2
---	0.05<W	As round type

AQL: 2.5

| 04 Polarizer bubbles | If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction. | | AQL 2.5 |

Size Φ	Acceptable Q TY
$\Phi \leqq 0.20$	Accept no dense
$0.20 < \Phi \leqq 0.50$	3
$0.50 < \Phi \leqq 1.00$	2
$1.00 < \Phi$	0
Total Q TY	3

NO	Item	Criterion	AQL
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination	
06	Chipped glass	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip : 6.1.1 Chip on panel surface and crack between panels: 表格如下： | z: Chip thickness | y: Chip width | x: Chip length | | $Z \leqq 1/2t$ | Not over viewing area | $x \leqq 1/8a$ | | $1/2t < z \leqq 2t$ | Not exceed 1/3k | $x \leqq 1/8a$ | ⊙If there are 2 or more chips, x is total length of each chip. 6.1.2 Corner crack: | z: Chip thickness | y: Chip width | x: Chip length | | $Z \leqq 1/2t$ | Not over viewing area | $x \leqq 1/8a$ | | $1/2t < z \leqq 2t$ | Not exceed 1/3k | $x \leqq 1/8a$ | ⊙If there are 2 or more chips, x is the total length of each chip.	2.5

6.1.1 table:

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed 1/3k	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is total length of each chip.

6.1.2 table:

z: Chip thickness	y: Chip width	x: Chip length
$Z \leqq 1/2t$	Not over viewing area	$x \leqq 1/8a$
$1/2t < z \leqq 2t$	Not exceed 1/3k	$x \leqq 1/8a$

⊙If there are 2 or more chips, x is the total length of each chip.

NO	Item	Criterion	AQL
06	Glass crack	Symbols : x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.2 Protrusion over terminal : 6.2.1 Chip on electrode pad : | y: Chip width	x: Chip length	z: Chip thickness
---	---	---
$y \leqq 0.5mm$	$x \leqq 1/8a$	$0 < z \leqq t$ | 6.2.2 Non-conductive portion: | y: Chip width	x: Chip length	z: Chip thickness
---	---	---
$y \leqq L$	$x \leqq 1/8a$	$0 < z \leqq t$ | ⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications. ⊙If the product will be heat sealed by the customer, the alignment mark not be damaged. 6.2.3 Substrate protuberance and internal crack. 	y: width	x: length
---	---
$y \leqq 1/3L$	$x \leqq a$		2.5

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB **X * Y<=2mm^2**	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	2.5
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65

14. <u>Material List of Components for RoHs</u>

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark "#"in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A：The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

2.Process for RoHS requirement：

(1) Use the Sn/Ag/Cu soldering surface；the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp.：

Reflow：250℃,30 seconds Max.；

Connector soldering wave or hand soldering：320℃, 10 seconds max.

(3) Temp. curve of reflow, max. Temp.：235±5℃ ；

Recommended customer's soldering temp. of connector：280℃, 3 seconds.

winstar   **LCM Sample Estimate Feedback Sheet**

Module Number：_____   **Page: 1**

1、Panel Specification：

1. Panel Type： ☐ Pass ☐ NG ,_____
2. View Direction： ☐ Pass ☐ NG ,_____
3. Numbers of Dots： ☐ Pass ☐ NG ,_____
4. View Area： ☐ Pass ☐ NG ,_____
5. Active Area： ☐ Pass ☐ NG ,_____
6. Operating Temperature： ☐ Pass ☐ NG ,_____
7. Storage Temperature： ☐ Pass ☐ NG ,_____
8. Others： _____

2、Mechanical Specification：

1. PCB Size： ☐ Pass ☐ NG ,_____
2. Frame Size： ☐ Pass ☐ NG ,_____
3. Materal of Frame： ☐ Pass ☐ NG ,_____
4. Connector Position： ☐ Pass ☐ NG ,_____
5. Fix Hole Position： ☐ Pass ☐ NG ,_____
6. Backlight Position： ☐ Pass ☐ NG ,_____
7. Thickness of PCB： ☐ Pass ☐ NG ,_____
8. Height of Frame to PCB： ☐ Pass ☐ NG ,_____
9. Height of Module： ☐ Pass ☐ NG ,_____
10. Others： ☐ Pass ☐ NG ,_____

3、Relative Hole Size：

1. Pitch of Connector： ☐ Pass ☐ NG ,_____
2. Hole size of Connector： ☐ Pass ☐ NG ,_____
3. Mounting Hole size： ☐ Pass ☐ NG ,_____
4. Mounting Hole Type： ☐ Pass ☐ NG ,_____
5. Others： ☐ Pass ☐ NG ,_____

4、Backlight Specification：

1. B/L Type： ☐ Pass ☐ NG ,_____
2. B/L Color： ☐ Pass ☐ NG ,_____
3. B/L Driving Voltage (Reference for LED Type)： ☐ Pass ☐ NG ,_____
4. B/L Driving Current： ☐ Pass ☐ NG ,_____
5. Brightness of B/L： ☐ Pass ☐ NG ,_____
6. B/L Solder Method： ☐ Pass ☐ NG ,_____
7. Others： ☐ Pass ☐ NG ,_____

＞＞ **Go to page 2** ＜＜

winstar

5、<u>**Electronic Characteristics of Module**</u>：

  1. Input Voltage：   ☐ Pass   ☐ NG , _____

  2. Supply Current：   ☐ Pass   ☐ NG , _____

  3. Driving Voltage for LCD：   ☐ Pass   ☐ NG , _____

  4. Contrast for LCD：   ☐ Pass   ☐ NG , _____

  5. B/L Driving Method：   ☐ Pass   ☐ NG , _____

  6. Negative Voltage Output：   ☐ Pass   ☐ NG , _____

  7. Interface Function：   ☐ Pass   ☐ NG , _____

  8. LCD Uniformity：   ☐ Pass   ☐ NG , _____

  9. ESD test：   ☐ Pass   ☐ NG , _____

10. Others：   ☐ Pass   ☐ NG , _____

6、<u>**Summary**</u>：

Sales signature： _____

Customer Signature： _____   **Date**： _____ / _____ / _____