



Winstar Display Co., LTD

華凌光電股份有限公司



住址: 407 台中市中清路 163 號
No.163 Chung Ching RD.,
Taichune, Taiwan, R.O.C

WEB: <http://www.winstar.com.tw>
E-mail: sales@winstar.com.tw
Tel:886-4-24262208 Fax : 886-4-24262207

SPECIFICATION

CUSTOMER : _____

MODULE NO.: WG320240E-TFI-NZ#

<p style="text-align: center;">APPROVED BY:</p> <p>(FOR CUSTOMER USE ONLY)</p>	<p>PCB VERSION: _____</p> <p>DATA: _____</p>
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SALES BY	APPROVED BY	CHECKED BY	PREPARED BY

VERSION	DATE	REVISED PAGE NO.	SUMMARY
	2011/02/12		First issue



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MODLE NO :

DOC. FIRST ISSUE

RECORDS OF REVISION

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0	2011/02/12		First issue

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1. Module Classification Information

W G 3 2 0 2 4 0 E - T F I - N Z #

① ② ③ ④ ⑤ ⑥ ⑦ ⑧

- ① Brand : WINSTAR DISPLAY CORPORATION
- ② Display Type : H→Character Type, G→Graphic Type
- ③ Display Font : 320 * 240 Dots
- ④ Model serials number
- ⑤ Backlight Type :

N→Without backlight	A→LED, Amber
B→EL, Blue green	R→LED, Red
D→EL, Green	O→LED, Orange
W→EL, White	G→LED, Green
F→CCFL, White	T→LED, White
Y→LED, Yellow Green	
- ⑥ LCD Mode :

B→TN Positive, Gray	T→FSTN Negative
N→TN Negative,	
G→STN Positive, Gray	
Y→STN Positive, Yellow Green	
M→STN Negative, Blue	
F→FSTN Positive	
- ⑦ LCD Polarize Type/
Temperature range/
View direction

A→Reflective, N.T, 6:00	H→Transflective, W.T,6:00
D→Reflective, N.T, 12:00	K→Transflective,W.T,12:00
G→Reflective, W. T, 6:00	C→Transmissive, N.T,6:00
J→Reflective, W. T, 12:00	F→Transmissive, N.T,12:00
B→Transflective, N.T,6:00	I→Transmissive, W. T, 6:00
E→Transflective, N.T.12:00	L→Transmissive, W.T,12:00
- ⑧ Special Code

N :Without Build in Negative Voltage ;
Z : IC NT7086 ;
#:Fit in with the ROHS Directions and regulations

2. Precautions in Use of LCD Module

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2) Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD Module.
- (3) Don't disassemble the LCM.
- (4) Don't operate it above the absolute maximum rating.
- (5) Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7) Storage: please storage in anti-static electricity container and clean environment.
- (8) Winstar have the right to change the passive components
(Resistors, capacitors and other passive components will have different appearance and color caused by the different supplier.)
- (9) Winstar have the right to change the PCB Rev.

3. General Specification

ITEM	STANDARD VALUE	UNIT
Number of dots	320x240	dots
Outline dimension	143.0(W)x 96.8(H)x 12.6max(T)	mm
View area	104.0(W)x 79.3(H)	mm
Active area	95.98(W)x 71.98(H)	mm
Dot size	0.28(W)x 0.28(H)	mm
Dot pitch	0.3(W)x 0.3(H)	mm
LCD type	FSTN Positive Transmissive, (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
View direction	6 o'clock	
Duty Method	1/240 Duty	
Backlight	LED, White	

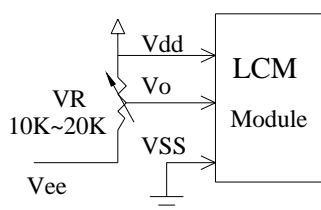
4. Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Operating Temperature	T_{OP}	-20	—	+70	°C
Storage Temperature	T_{ST}	-30	—	+80	°C
Input Voltage	V_I	0	—	5	V
Supply Voltage For Logic	V_{DD}	0	—	6.5	V
Supply Voltage For LCD	$V_{DD}-V_{EE}$	0	—	22.4	V

5. Electrical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Logic Voltage	$V_{DD}-V_{SS}$	$V_{DD}=3V,5V$	2.7		5.5	V
Supply Voltage For LCD	$V_{DD}-V_O$	$T_a=-20^{\circ}C$	—	—	—	V
		$T_a=25^{\circ}C$	23.5	24.2	24.9	V
		$T_a=+70^{\circ}C$	—	—	—	V
*Note						
Input High Volt.	V_{IH}	—	$0.8V_{DD}$	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	-0.3	—	$0.2V_{DD}$	V
Output High Volt.	V_{OH}	—	$V_{DD}-0.4$	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	0	—	0.4	V
Supply Current	I_{DD}	$V=5.0V$	30	45	50	mA

* Note: Please design the VOP adjustment circuit on customer's main board

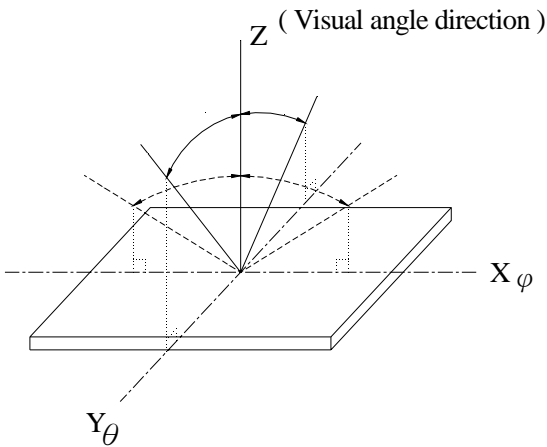


6. Optical Characteristics

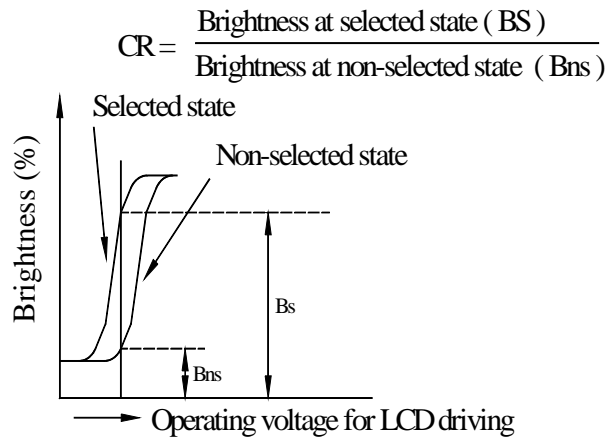
ITEM	SYMBOL	CONDITION	MIN	TYP	MAX	UNIT
View Angle	(V) θ	$CR \geq 3$	30	—	60	deg.
	(H) φ	$CR \geq 3$	-45	—	45	deg.
Contrast Ratio	CR	—	—	5	—	—
Response Time	T rise	—	—	200	300	ms
	T fall	—	—	150	200	ms

6.1 Definitions

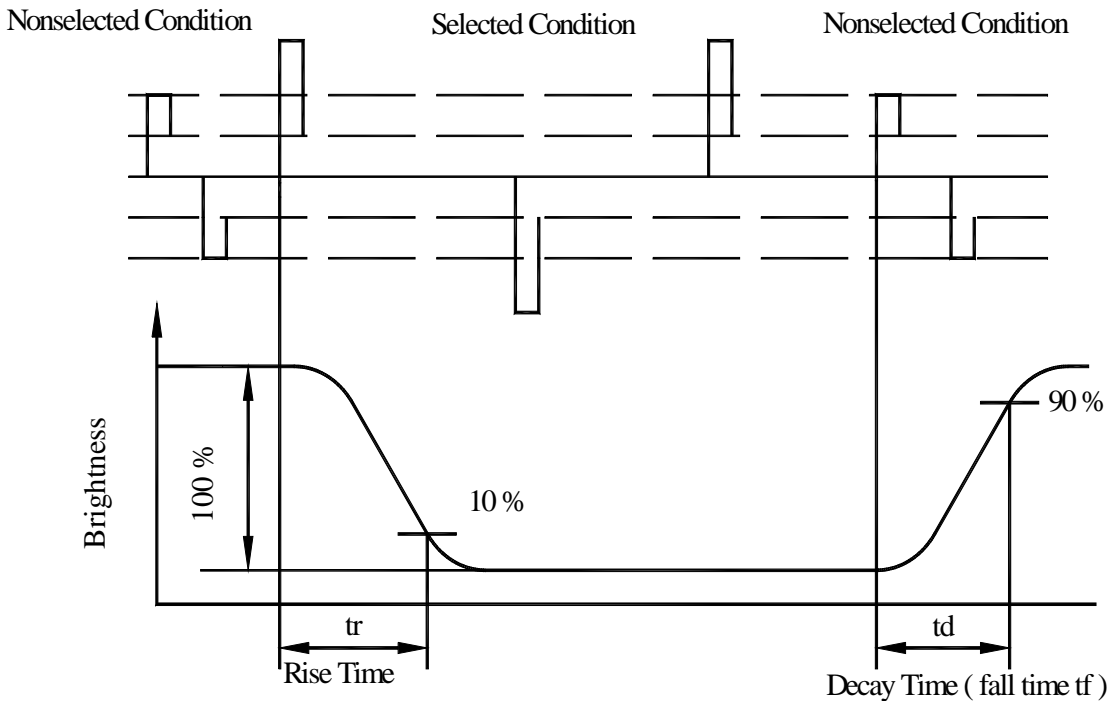
■ View Angles



■ Contrast Ratio



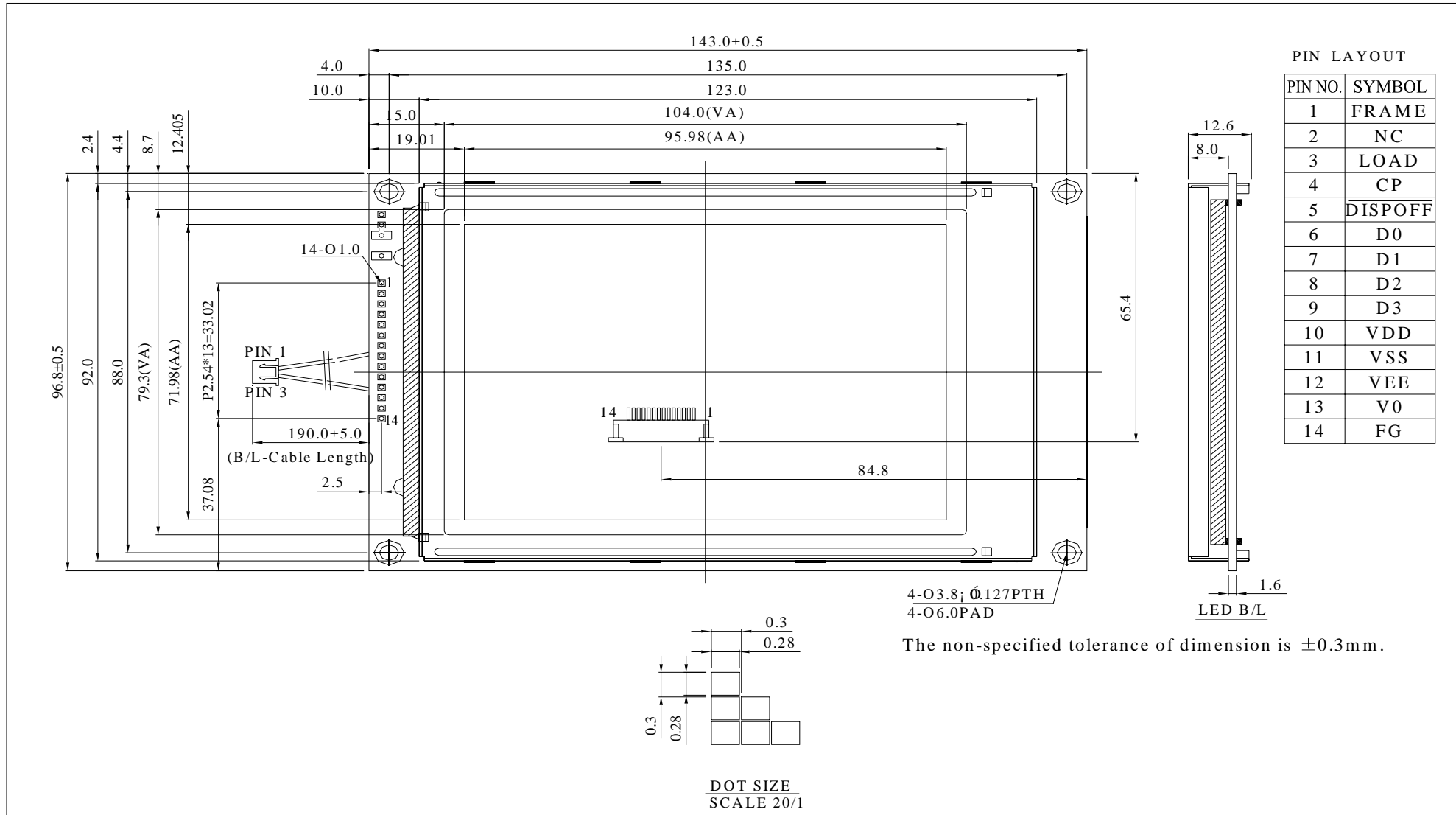
■ Response time

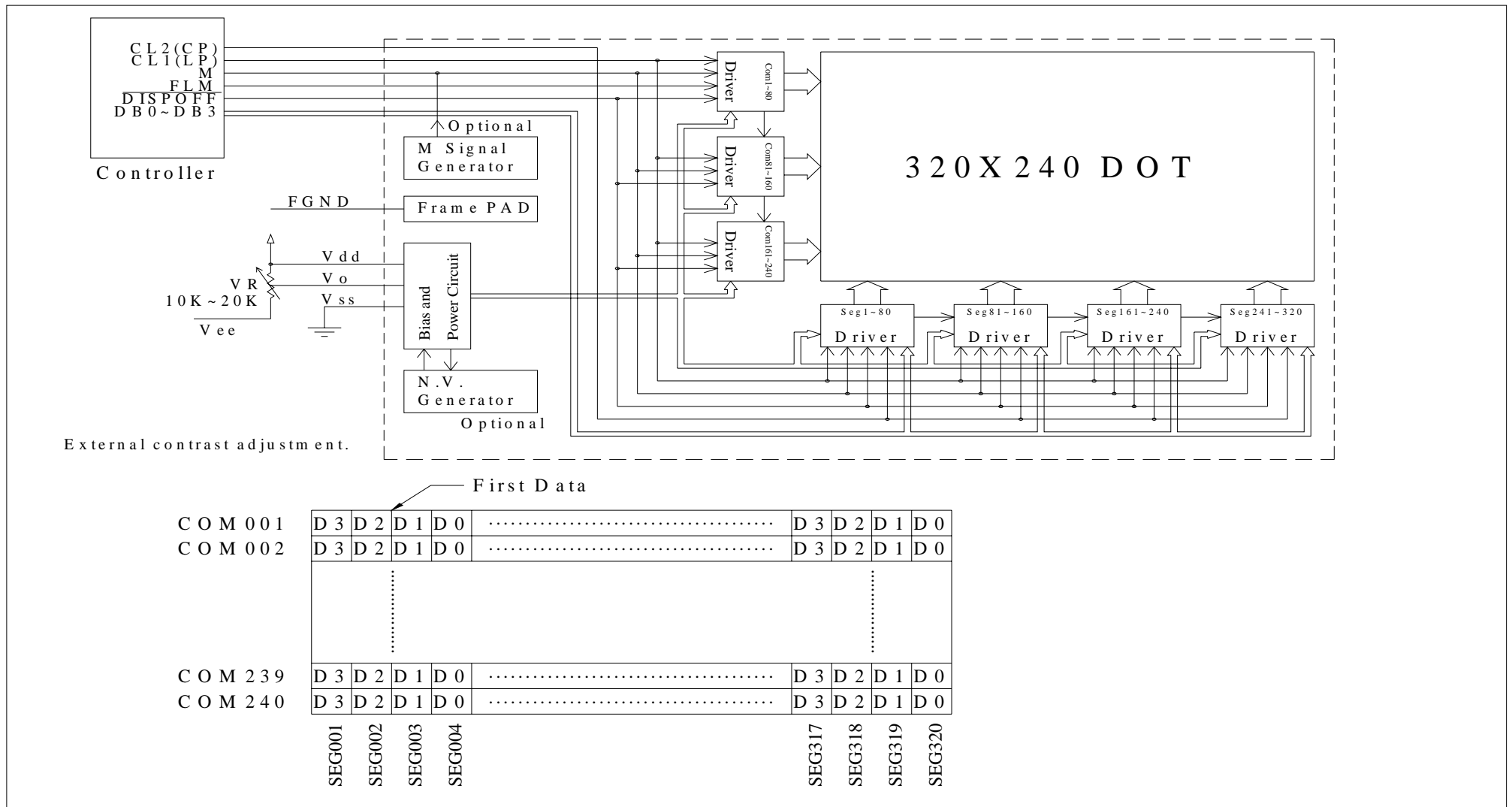


7. Interface Description

Pin No.	Symbol	Level	Description
1	FRAME	H/L	Scan start-up signal
2	NC	H/L	NO connection
3	LOAD(CL1)	H to L	Data latch pulse
4	CP(CL2)	H to L	Data shift pulse
5	$\overline{\text{DISPOFF}}$	H/L	H: Display ON, L: Display OFF
6	D0	H/L	Data bus line
7	D1	H/L	Data bus line
8	D2	H/L	Data bus line
9	D3	H/L	Data bus line
10	V _{DD}	5.0V	Power supply for Logic
11	V _{SS}	0V	Ground
12	V _{EE}		NC
13	V _O	(Variable)	Driving voltage for LCD
14	FG		Frame Ground

8. Contour Drawing & Block diagram





9. Timing Characteristics

9.1. Common & Segment interface timing:

ITEM	symbol	Test Condition	Min.	Typ.	Max.	Units
Clock Cycle	tC	Fig.1	100	—	—	ns
CP Pulse Width	tWC	Fig.1	50	—	—	ns
LP Pulse Width	tWL	Fig.1	50	—	—	ns
Data Set Up Time	tDSU	Fig.1	30	—	—	ns
Data Hold Time	tDHD	Fig.1	30	—	—	ns
CP Rise/Fall Time	tr,tf	Fig.1	—	—	50	ns
CP to LOAD	tCL	Fig.1	80	—	—	ns
LOAD to CP	tLC	Fig.1	110	—	—	ns
LP Pulse Width	tLW	Fig.1	50	—	—	ns
CL1 Pulse Width	tCW	Fig.2	63	—	—	ns
Data Set Up Time	tDSU2	Fig.2	100	—	—	ns
Data Hold Time	tDHD2	Fig.2	100	—	—	ns
CL1 Rise/Fall Time	tr2,tf2	Fig.2	—	—	50	ns

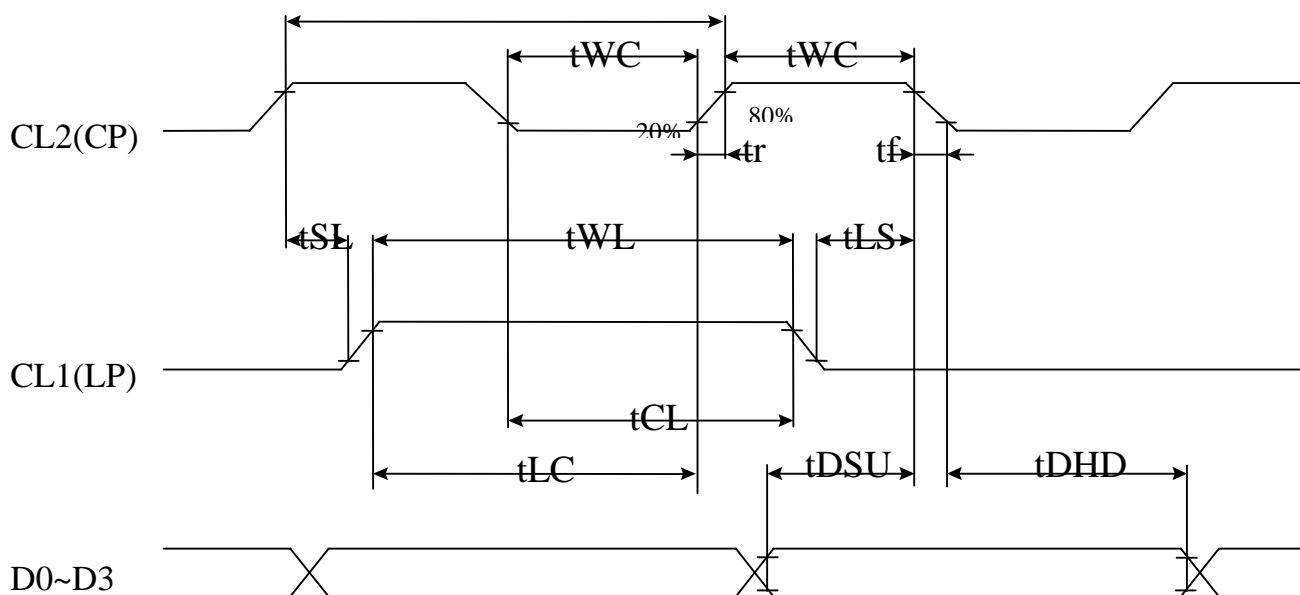


Fig 1. SEGMENT TIMING

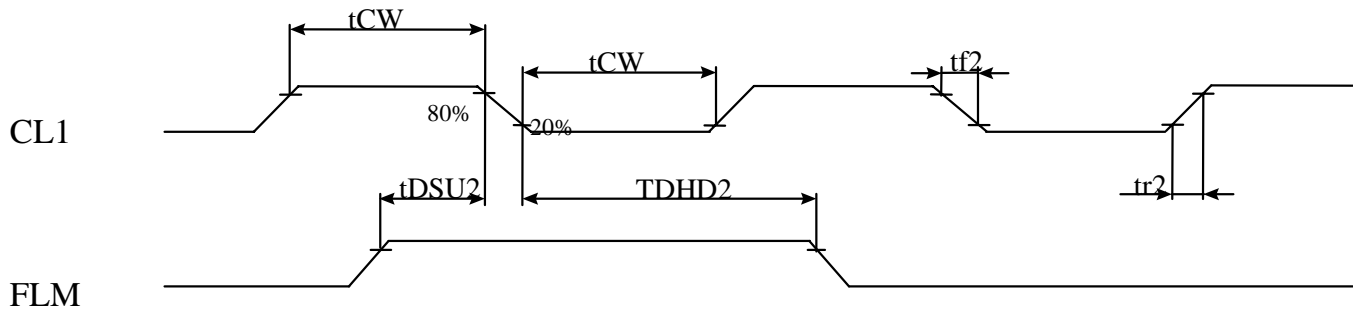
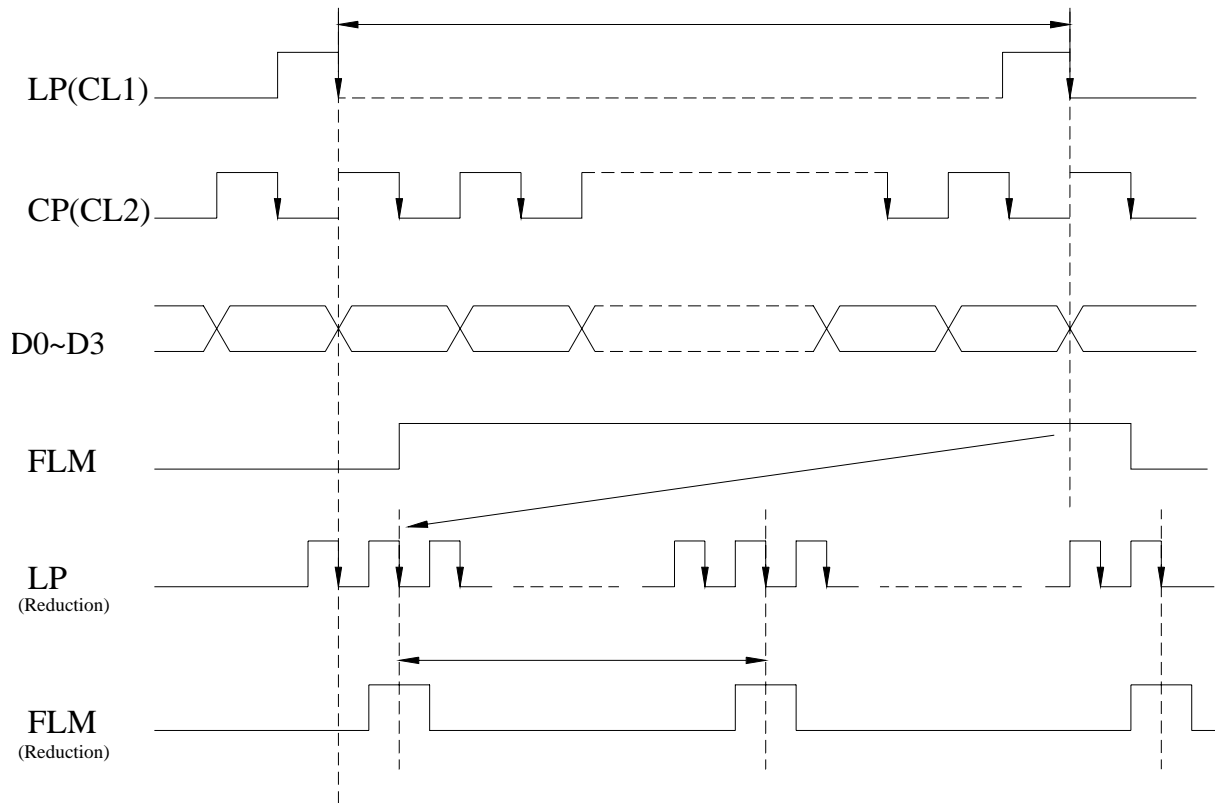
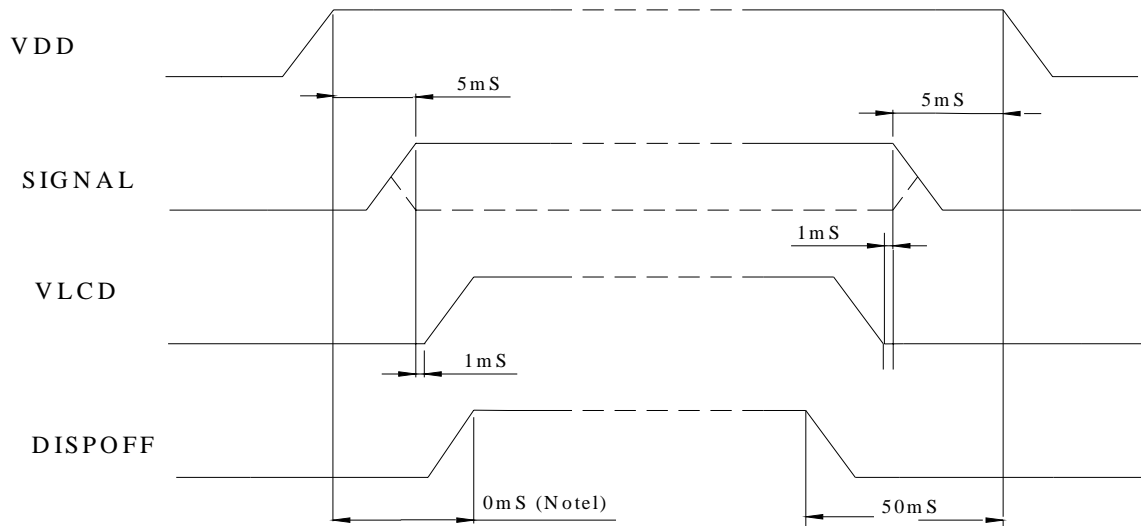


Fig 2 COMMON TIMING

TIMING CHART OF INPUT SIGNAL



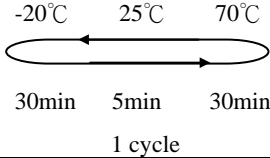
POWER ON/OFF TIMING



*Note1: The delay time of /Dispoff signal can be 0ms within VDD input. Owing to having good display quality, /Dispoff signal have to delay at least 10ms or more for waiting power in stable.

10. RELIABILITY

Content of Reliability Test (wide temperature, -20°C~70°C)

Environmental Test			
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	—
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60°C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation  -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°C/70°C 10 cycles	—
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude : 1.5mm Vibration Frequency : 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5kΩ CS=100pF 1 time	—

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: Vibration test will be conducted to the product itself without putting it in a container.

11. Backlight Information

LED(Edge) backlight Specification

(Ta=25°C)

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	I _{LED}	115.2	128	200	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	—
Reverse Voltage	V _R	—	—	5	V	—
Luminous Intensity	I _V	267.7	334.7	—	CD/M ²	I _{LED} =128mA
LED Life Time (For Reference only)	—	—	50K	—	Hr.	I _{LED} =128mA 25°C,50-60%RH, (Note 1)
Color	White					

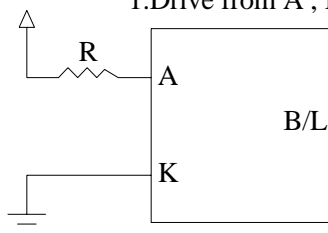
Note: The LED of B/L is drive by current only, drive voltage is for reference only.

drive voltage can make driving current under safety area (current between minimum and maximum).

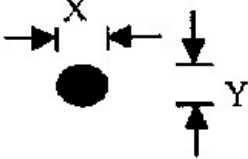
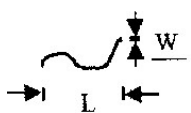
Note 1:50K hours is only an estimate for reference.

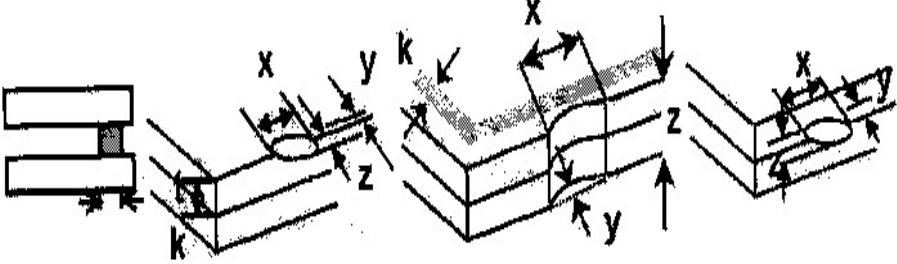
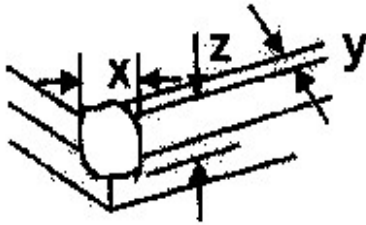
LED B\L Drive Method

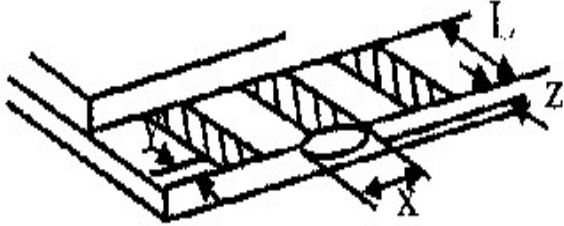
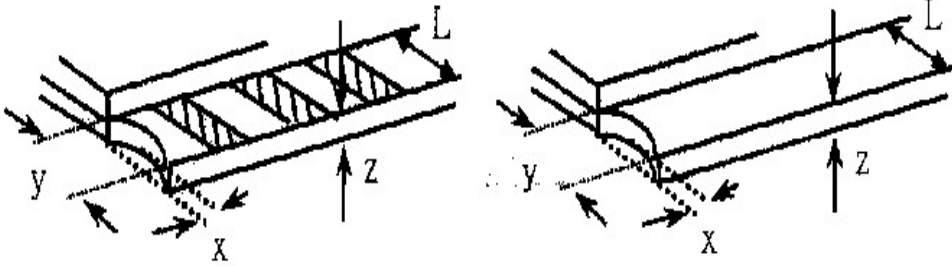
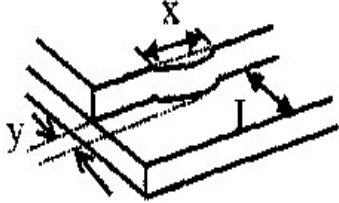
1.Drive from A , K

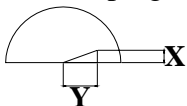


12. Inspection specification

NO	Item	Criterion	AQL												
01	Electrical Testing	1.1 Missing vertical, horizontal segment, segment contrast defect. 1.2 Missing character , dot or icon. 1.3 Display malfunction. 1.4 No function or no display. 1.5 Current consumption exceeds product specifications. 1.6 LCD viewing angle defect. 1.7 Mixed product types. 1.8 Contrast defect.	0.65												
02	Black or white spots on LCD (display only)	2.1 White and black spots on display $\leq 0.25\text{mm}$, no more than three white or black spots present. 2.2 Densely spaced: No more than two spots or lines within 3mm	2.5												
03	LCD black spots, white spots, contamination (non-display)	3.1 Round type : As following drawing $\Phi = (x + y) / 2$  <table border="1" data-bbox="853 929 1332 1142"> <thead> <tr> <th>SIZE</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.10$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.10 < \Phi \leq 0.20$</td> <td>2</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.25$</td> <td>1</td> </tr> <tr> <td>$0.25 < \Phi$</td> <td>0</td> </tr> </tbody> </table>	SIZE	Acceptable Q TY	$\Phi \leq 0.10$	Accept no dense	$0.10 < \Phi \leq 0.20$	2	$0.20 < \Phi \leq 0.25$	1	$0.25 < \Phi$	0	2.5		
		SIZE	Acceptable Q TY												
$\Phi \leq 0.10$	Accept no dense														
$0.10 < \Phi \leq 0.20$	2														
$0.20 < \Phi \leq 0.25$	1														
$0.25 < \Phi$	0														
3.2 Line type : (As following drawing)  <table border="1" data-bbox="694 1220 1332 1433"> <thead> <tr> <th>Length</th> <th>Width</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>---</td> <td>$W \leq 0.02$</td> <td>Accept no dense</td> </tr> <tr> <td>$L \leq 3.0$</td> <td>$0.02 < W \leq 0.03$</td> <td rowspan="2">2</td> </tr> <tr> <td>$L \leq 2.5$</td> <td>$0.03 < W \leq 0.05$</td> </tr> <tr> <td>---</td> <td>$0.05 < W$</td> <td>As round type</td> </tr> </tbody> </table>	Length	Width	Acceptable Q TY	---	$W \leq 0.02$	Accept no dense	$L \leq 3.0$	$0.02 < W \leq 0.03$	2	$L \leq 2.5$	$0.03 < W \leq 0.05$	---	$0.05 < W$	As round type	2.5
Length	Width	Acceptable Q TY													
---	$W \leq 0.02$	Accept no dense													
$L \leq 3.0$	$0.02 < W \leq 0.03$	2													
$L \leq 2.5$	$0.03 < W \leq 0.05$														
---	$0.05 < W$	As round type													
04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction. <table border="1" data-bbox="821 1478 1332 1736"> <thead> <tr> <th>Size Φ</th> <th>Acceptable Q TY</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.20$</td> <td>Accept no dense</td> </tr> <tr> <td>$0.20 < \Phi \leq 0.50$</td> <td>3</td> </tr> <tr> <td>$0.50 < \Phi \leq 1.00$</td> <td>2</td> </tr> <tr> <td>$1.00 < \Phi$</td> <td>0</td> </tr> <tr> <td>Total Q TY</td> <td>3</td> </tr> </tbody> </table>	Size Φ	Acceptable Q TY	$\Phi \leq 0.20$	Accept no dense	$0.20 < \Phi \leq 0.50$	3	$0.50 < \Phi \leq 1.00$	2	$1.00 < \Phi$	0	Total Q TY	3	2.5
Size Φ	Acceptable Q TY														
$\Phi \leq 0.20$	Accept no dense														
$0.20 < \Phi \leq 0.50$	3														
$0.50 < \Phi \leq 1.00$	2														
$1.00 < \Phi$	0														
Total Q TY	3														

NO	Item	Criterion	AQL																		
05	Scratches	Follow NO.3 LCD black spots, white spots, contamination																			
06	Chipped glass	<p>Symbols Define:</p> <p>x: Chip length y: Chip width z: Chip thickness</p> <p>k: Seal width t: Glass thickness a: LCD side length</p> <p>L: Electrode pad length:</p> <p>6.1 General glass chip :</p> <p>6.1.1 Chip on panel surface and crack between panels:</p>  <table border="1" data-bbox="427 1205 1337 1400"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </table> <p>⊙If there are 2 or more chips, x is total length of each chip.</p> <p>6.1.2 Corner crack:</p>  <table border="1" data-bbox="427 1883 1337 2078"> <tr> <td>z: Chip thickness</td> <td>y: Chip width</td> <td>x: Chip length</td> </tr> <tr> <td>$Z \leq 1/2t$</td> <td>Not over viewing area</td> <td>$x \leq 1/8a$</td> </tr> <tr> <td>$1/2t < z \leq 2t$</td> <td>Not exceed 1/3k</td> <td>$x \leq 1/8a$</td> </tr> </table> <p>⊙If there are 2 or more chips, x is the total length of each chip.</p>	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	z: Chip thickness	y: Chip width	x: Chip length	$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$	$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$	2.5
z: Chip thickness	y: Chip width	x: Chip length																			
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																			
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$																			
z: Chip thickness	y: Chip width	x: Chip length																			
$Z \leq 1/2t$	Not over viewing area	$x \leq 1/8a$																			
$1/2t < z \leq 2t$	Not exceed 1/3k	$x \leq 1/8a$																			

NO	Item	Criterion	AQL																
06	Glass crack	<p>Symbols :</p> <p>x: Chip length y: Chip width z: Chip thickness</p> <p>k: Seal width t: Glass thickness a: LCD side length</p> <p>L: Electrode pad length</p> <p>6.2 Protrusion over terminal :</p> <p>6.2.1 Chip on electrode pad :</p>  <table border="1" data-bbox="336 853 1249 983"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq 0.5\text{mm}$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>6.2.2 Non-conductive portion:</p>  <table border="1" data-bbox="410 1337 1249 1467"> <tr> <td>y: Chip width</td> <td>x: Chip length</td> <td>z: Chip thickness</td> </tr> <tr> <td>$y \leq L$</td> <td>$x \leq 1/8a$</td> <td>$0 < z \leq t$</td> </tr> </table> <p>⊙If the chipped area touches the ITO terminal, over 2/3 of the ITO must remain and be inspected according to electrode terminal specifications.</p> <p>⊙If the product will be heat sealed by the customer, the alignment mark not be damaged.</p> <p>6.2.3 Substrate protuberance and internal crack.</p>  <table border="1" data-bbox="746 1803 1254 1933"> <tr> <td>y: width</td> <td>x: length</td> </tr> <tr> <td>$y \leq 1/3L$</td> <td>$x \leq a$</td> </tr> </table>	y: Chip width	x: Chip length	z: Chip thickness	$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$	y: Chip width	x: Chip length	z: Chip thickness	$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$	y: width	x: length	$y \leq 1/3L$	$x \leq a$	2.5
y: Chip width	x: Chip length	z: Chip thickness																	
$y \leq 0.5\text{mm}$	$x \leq 1/8a$	$0 < z \leq t$																	
y: Chip width	x: Chip length	z: Chip thickness																	
$y \leq L$	$x \leq 1/8a$	$0 < z \leq t$																	
y: width	x: length																		
$y \leq 1/3L$	$x \leq a$																		

NO	Item	Criterion	AQL
07	Cracked glass	The LCD with extensive crack is not acceptable.	2.5
08	Backlight elements	8.1 Illumination source flickers when lit. 8.2 Spots or scratched that appear when lit must be judged. Using LCD spot, lines and contamination standards. 8.3 Backlight doesn't light or color wrong.	0.65 2.5 0.65
09	Bezel	9.1 Bezel may not have rust, be deformed or have fingerprints, stains or other contamination. 9.2 Bezel must comply with job specifications.	2.5 0.65
10	PCB、COB	10.1 COB seal may not have pinholes larger than 0.2mm or contamination. 10.2 COB seal surface may not have pinholes through to the IC. 10.3 The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB  $X * Y \leq 2\text{mm}^2$	2.5 2.5 0.65 2.5 2.5 0.65 0.65 2.5 2.5
11	Soldering	11.1 No un-melted solder paste may be present on the PCB. 11.2 No cold solder joints, missing solder connections, oxidation or icicle. 11.3 No residue or solder balls on PCB. 11.4 No short circuits in components on PCB.	2.5 2.5 2.5 0.65

NO	Item	Criterion	AQL
12	General appearance	12.1 No oxidation, contamination, curves or, bends on interface Pin (OLB) of TCP.	2.5
		12.2 No cracks on interface pin (OLB) of TCP.	0.65
		12.3 No contamination, solder residue or solder balls on product.	2.5
		12.4 The IC on the TCP may not be damaged, circuits.	2.5
		12.5 The uppermost edge of the protective strip on the interface pin must be present or look as if it cause the interface pin to sever.	2.5
		12.6 The residual rosin or tin oil of soldering (component or chip component) is not burned into brown or black color.	2.5
		12.7 Sealant on top of the ITO circuit has not hardened.	2.5
		12.8 Pin type must match type in specification sheet.	0.65
		12.9 LCD pin loose or missing pins.	0.65
		12.10 Product packaging must the same as specified on packaging specification sheet.	0.65
		12.11 Product dimension and structure must conform to product specification sheet.	0.65

13. Material List of Components for RoHs

1. WINSTAR Display Co., Ltd hereby declares that all of or part of products (with the mark “#”in code), including, but not limited to, the LCM, accessories or packages, manufactured and/or delivered to your company (including your subsidiaries and affiliated company) directly or indirectly by our company (including our subsidiaries or affiliated companies) do not intentionally contain any of the substances listed in all applicable EU directives and regulations, including the following substances.

Exhibit A : The Harmful Material List

Material	(Cd)	(Pb)	(Hg)	(Cr6+)	PBBs	PBDEs
Limited Value	100 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm	1000 ppm
Above limited value is set up according to RoHS.						

2.Process for RoHS requirement :

(1) Use the Sn/Ag/Cu soldering surface ; the surface of Pb-free solder is rougher than we used before.

(2) Heat-resistance temp. :

Reflow : 250°C,30 seconds Max. ;

Connector soldering wave or hand soldering : 320°C, 10 seconds max.

(3) Temp. curve of reflow, max. Temp. : 235±5°C ;

Recommended customer's soldering temp. of connector : 280°C, 3 seconds.

16. Recommendable storage

1. Place the panel or module in the temperature 25°C±5°C and the humidity below 65% RH
2. Do not place the module near organics solvents or corrosive gases.
3. Do not crush, shake, or jolt the module

**1、Panel Specification :**

1. Panel Type : Pass NG , _____
2. View Direction : Pass NG , _____
3. Numbers of Dots : Pass NG , _____
4. View Area : Pass NG , _____
5. Active Area : Pass NG , _____
6. Operating Temperature : Pass NG , _____
7. Storage Temperature : Pass NG , _____
8. Others : _____

2、Mechanical Specification :

1. PCB Size : Pass NG , _____
2. Frame Size : Pass NG , _____
3. Material of Frame : Pass NG , _____
4. Connector Position : Pass NG , _____
5. Fix Hole Position : Pass NG , _____
6. Backlight Position : Pass NG , _____
7. Thickness of PCB : Pass NG , _____
8. Height of Frame to PCB : Pass NG , _____
9. Height of Module : Pass NG , _____
10. Others : Pass NG , _____

3、Relative Hole Size :

1. Pitch of Connector : Pass NG , _____
2. Hole size of Connector : Pass NG , _____
3. Mounting Hole size : Pass NG , _____
4. Mounting Hole Type : Pass NG , _____
5. Others : Pass NG , _____

4、Backlight Specification :

1. B/L Type : Pass NG , _____
2. B/L Color : Pass NG , _____
3. B/L Driving Voltage (Reference for LED Type) : Pass NG , _____
4. B/L Driving Current : Pass NG , _____
5. Brightness of B/L : Pass NG , _____
6. B/L Solder Method : Pass NG , _____
7. Others : Pass NG , _____

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5、Electronic Characteristics of Module :

- | | | |
|------------------------------|-------------------------------|-------------------------------------|
| 1. Input Voltage : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 2. Supply Current : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 3. Driving Voltage for LCD : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 4. Contrast for LCD : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 5. B/L Driving Method : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 6. Negative Voltage Output : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 7. Interface Function : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 8. LCD Uniformity : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 9. ESD test : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |
| 10. Others : | <input type="checkbox"/> Pass | <input type="checkbox"/> NG , _____ |

6、Summary :

Sales signature : _____

Customer Signature : _____

Date : / /